

**E-mu Systems, Inc.**  
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# EMULATOR II

**SCHEMATICS**

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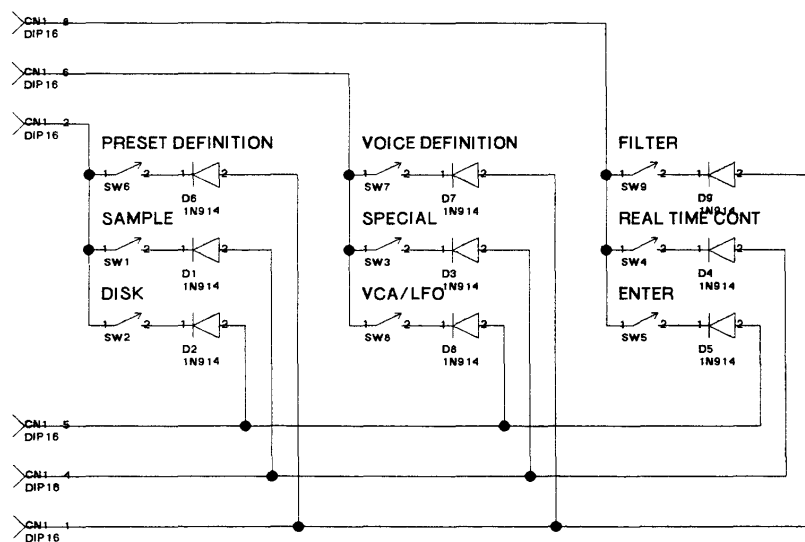
## **EMULATOR II SCHEMATICS**

### **CPU BOARD**

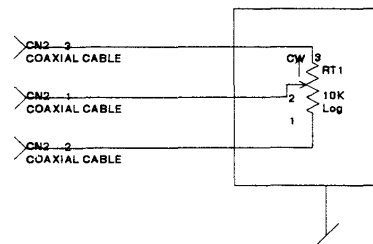
Scanner CPU	7 - 6
Main CPU	7 - 10
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Serial Interfaces	7 - 16
Microcontroller	7 - 17
RAM Timing	7 - 23
RAM Address Control	7 - 24
RAM Buffering	7 - 25
Dynamic RAM	7 - 26
Clocks and Reset	7 - 30
Power and Connectors	7 - 31
Piggyback Memory	7 - 32

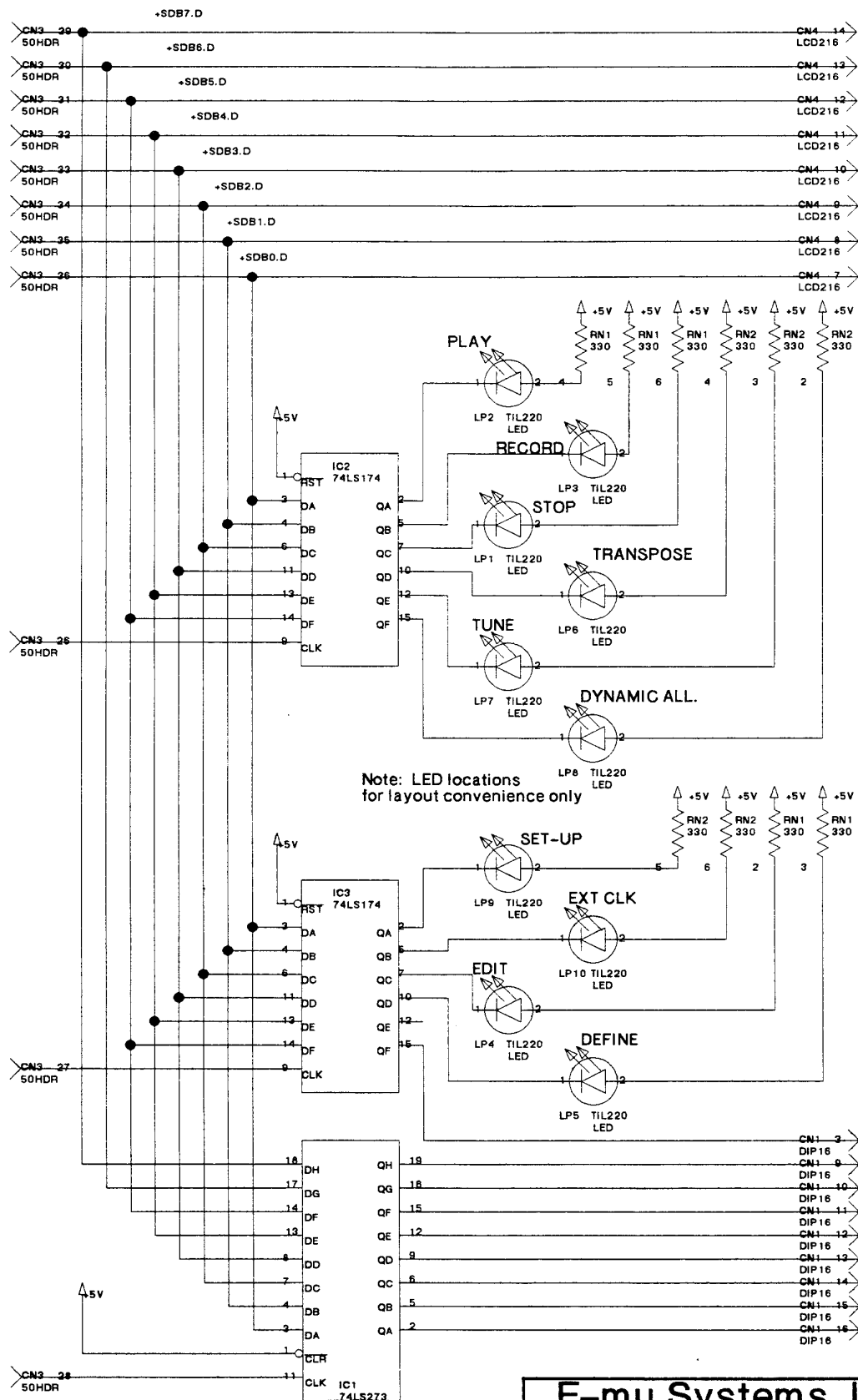
### **OUTPUT BOARD**

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Timers	7 - 40
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SAR	7 - 43
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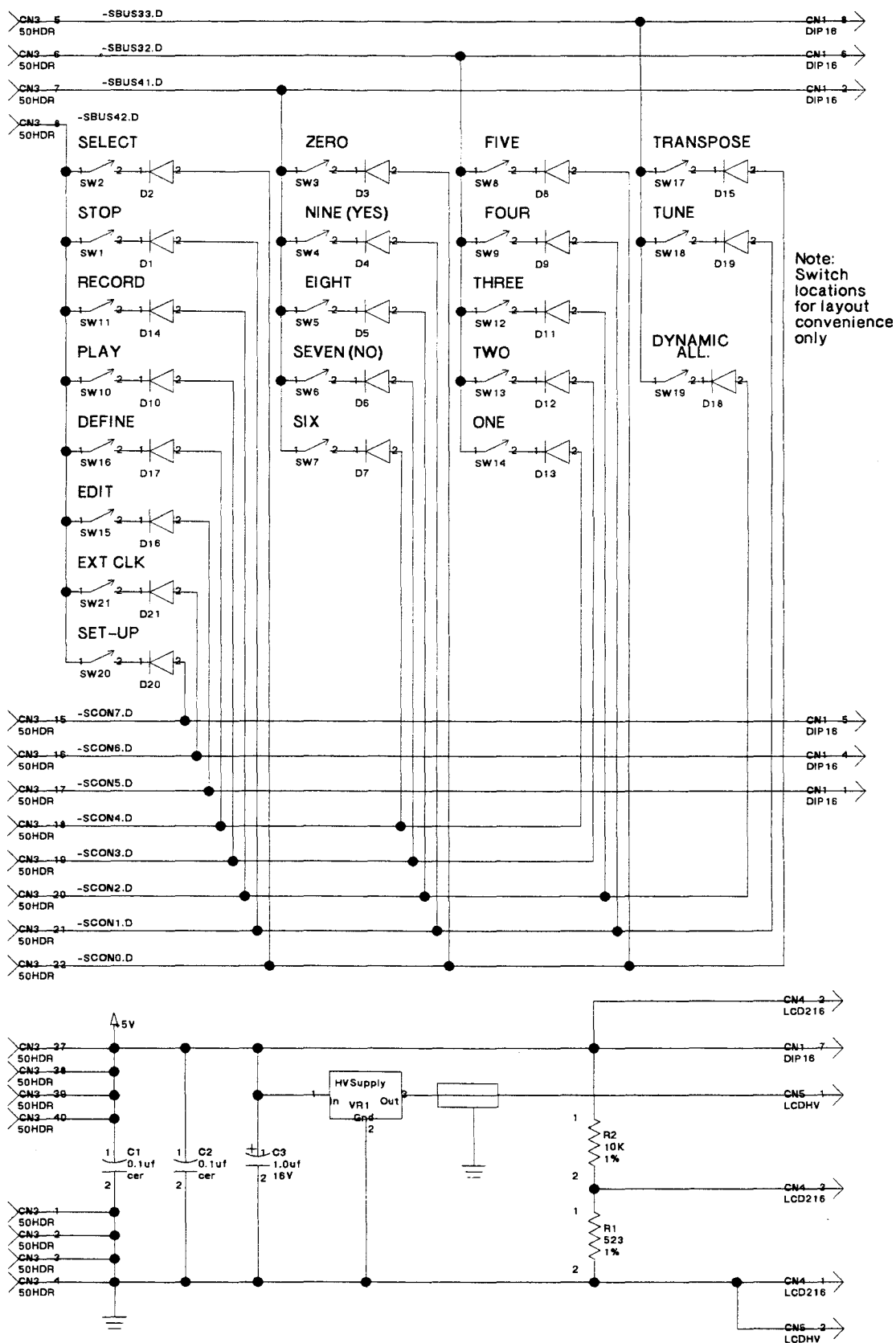
Note: LED locations for layout convenience only

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Emulator 2 Left Panel - LED drivers

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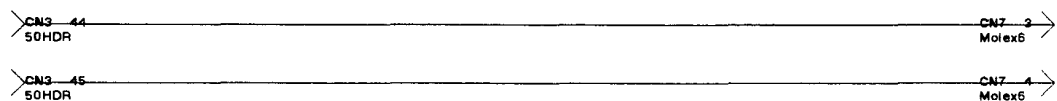
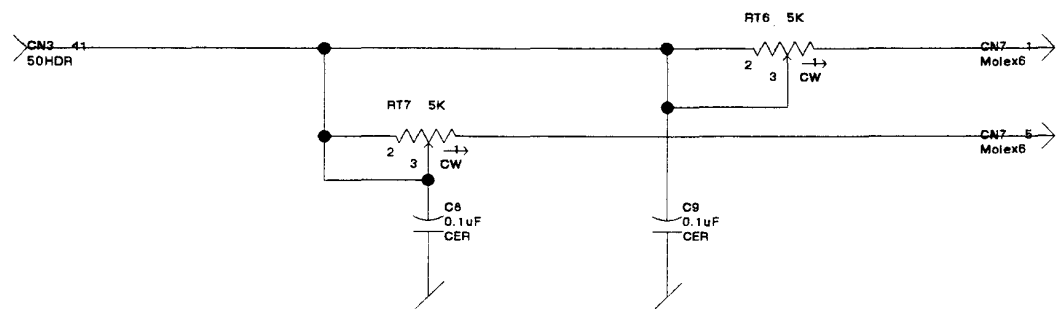
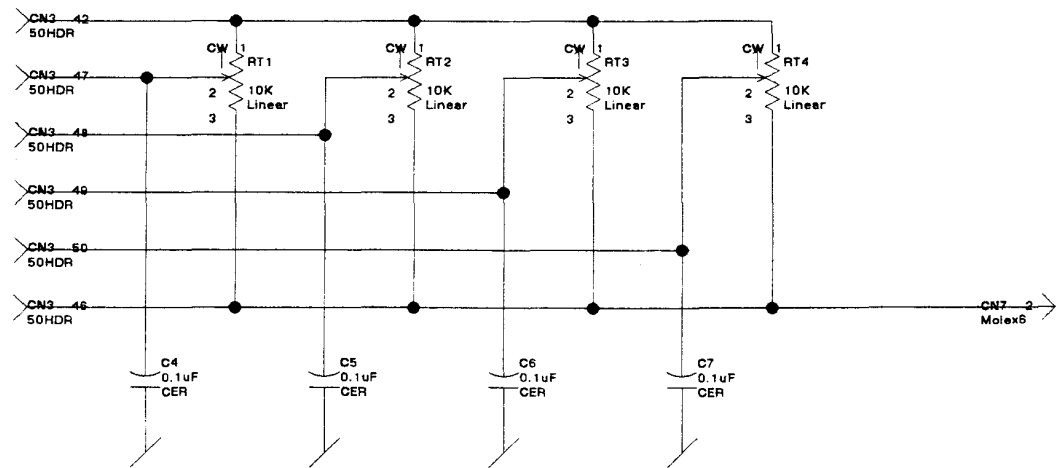


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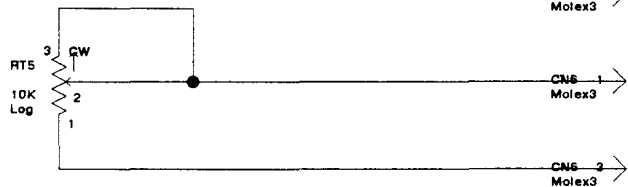
Emulator 2 Left Panel - PB's & supplies

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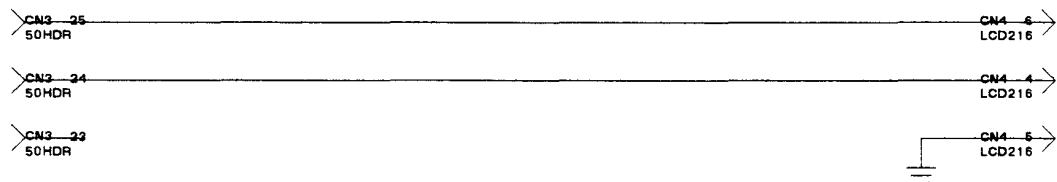
RT5 POT BODY  
IS GROUNDED



SCSWD

SA0

SA1

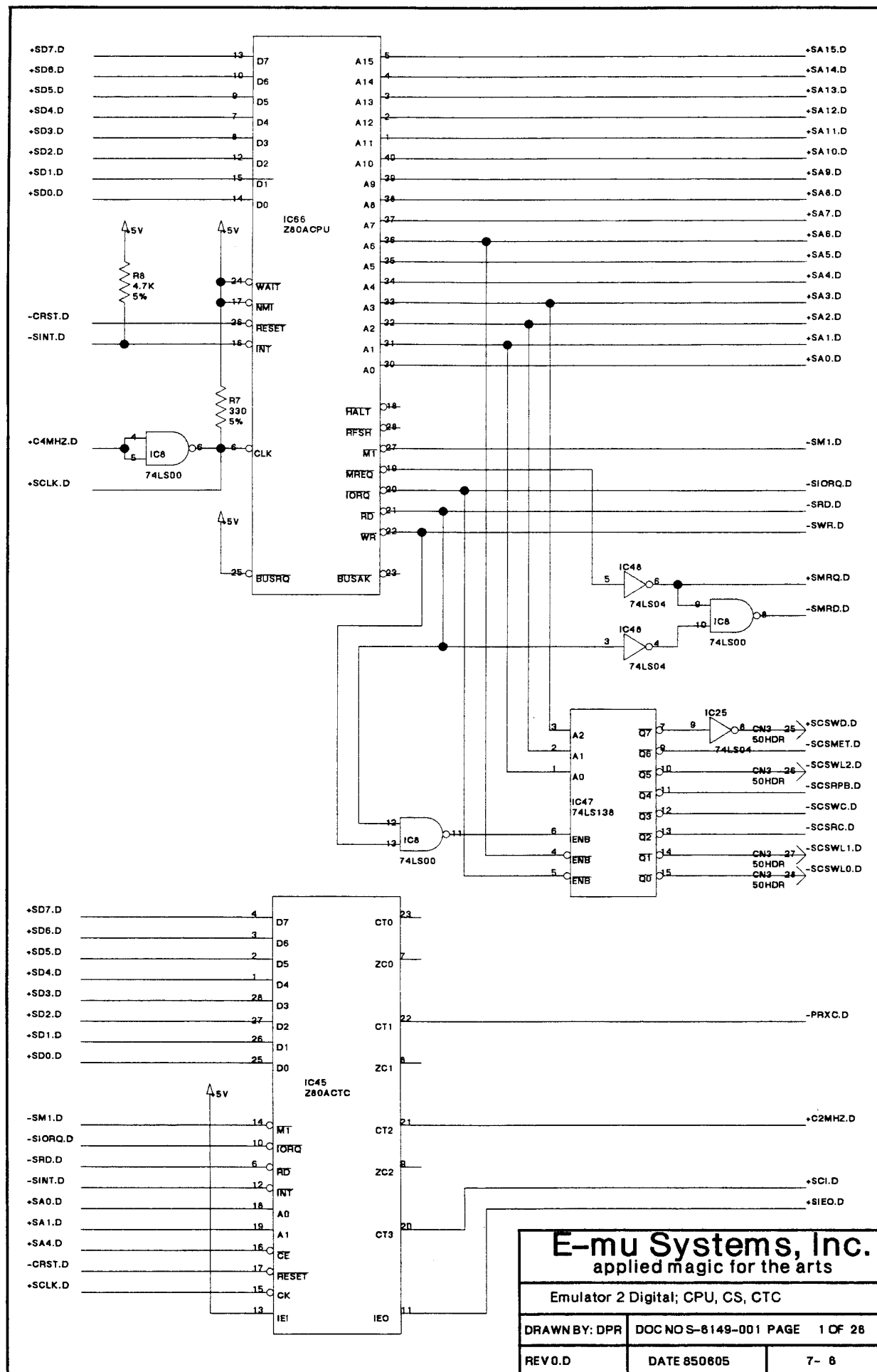


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Emulator 2 Left Panel - Sliders & Wheels

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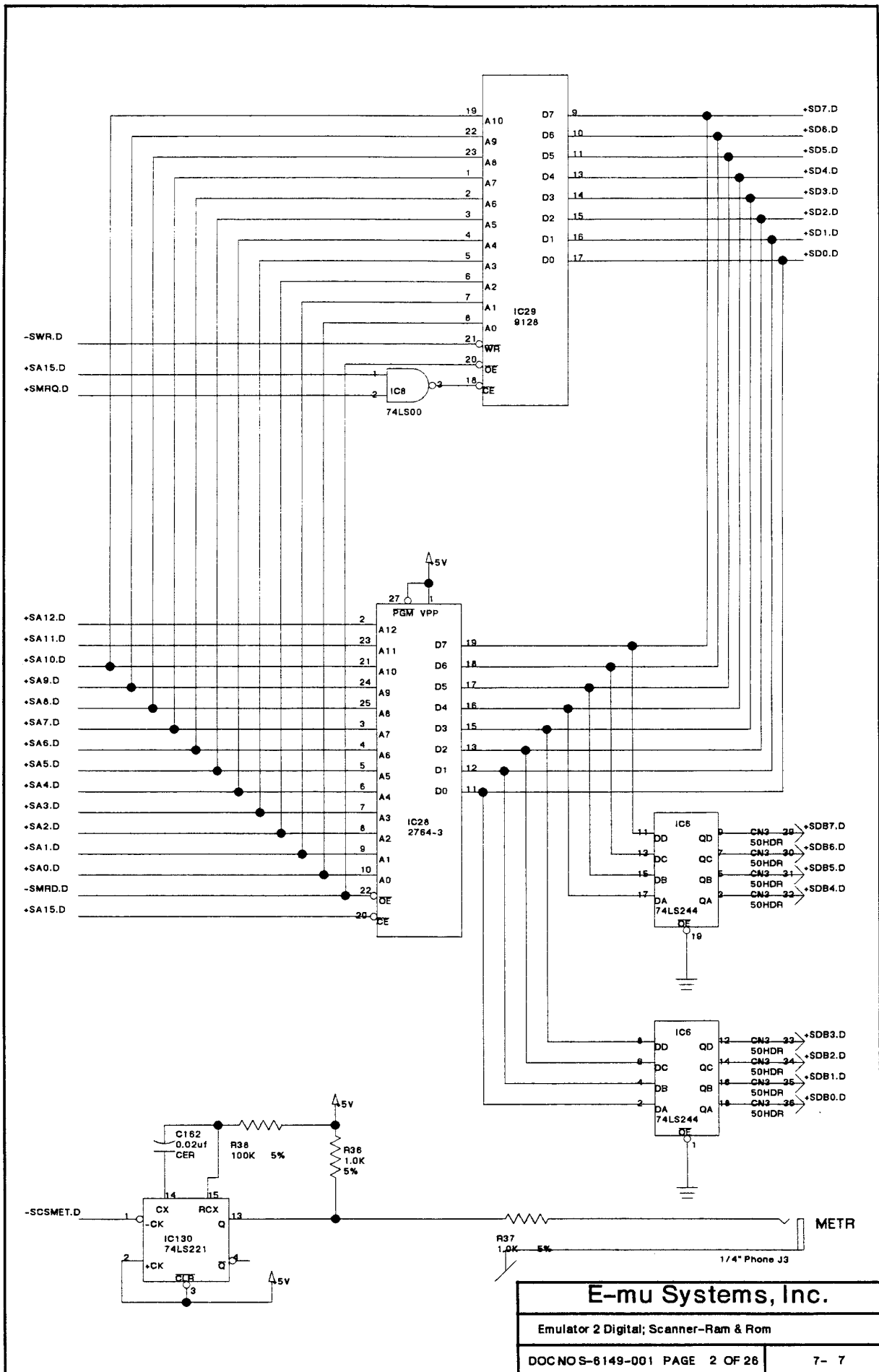


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Emulator 2 Digital; CPU, CS, CTC

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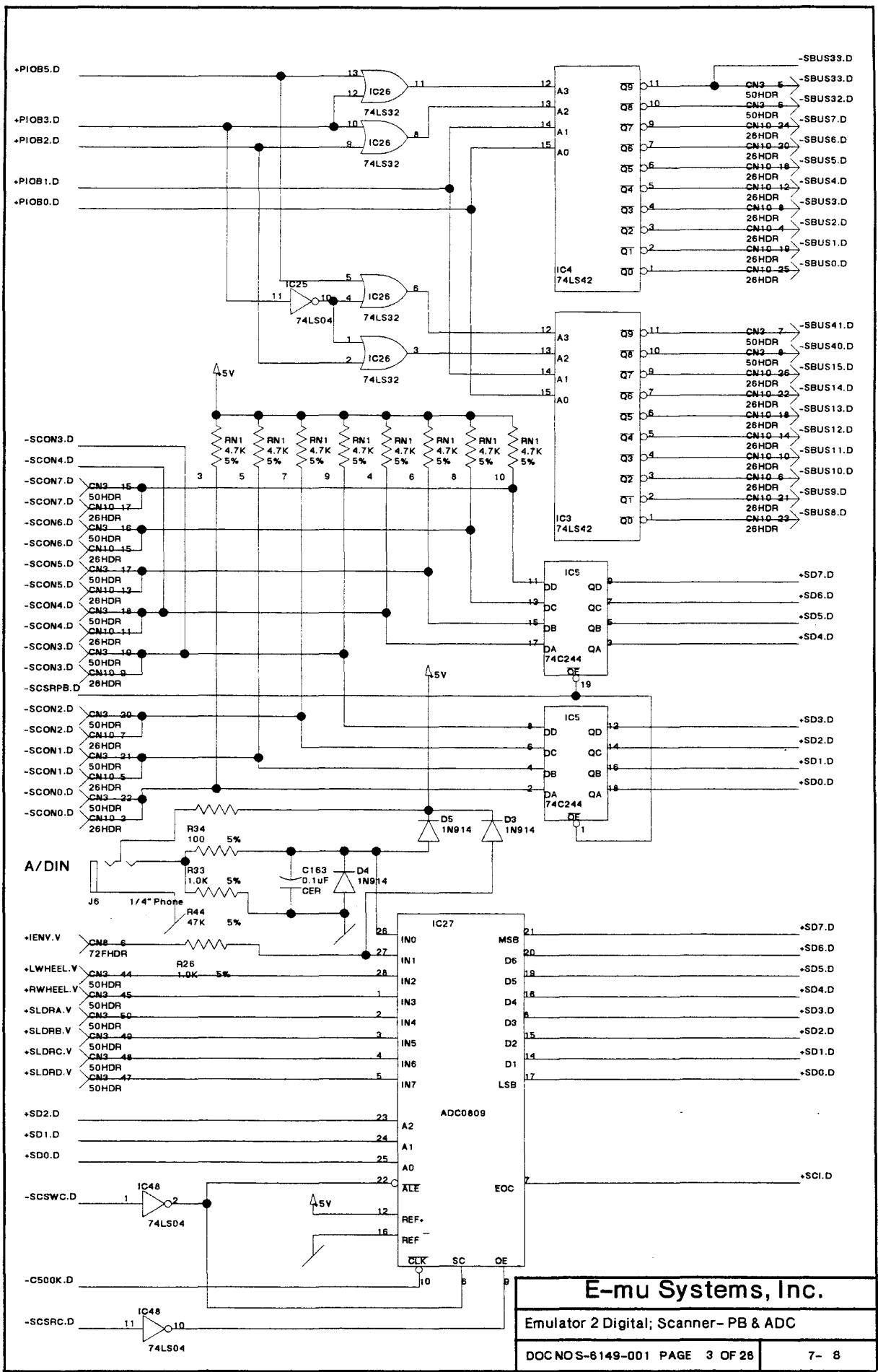
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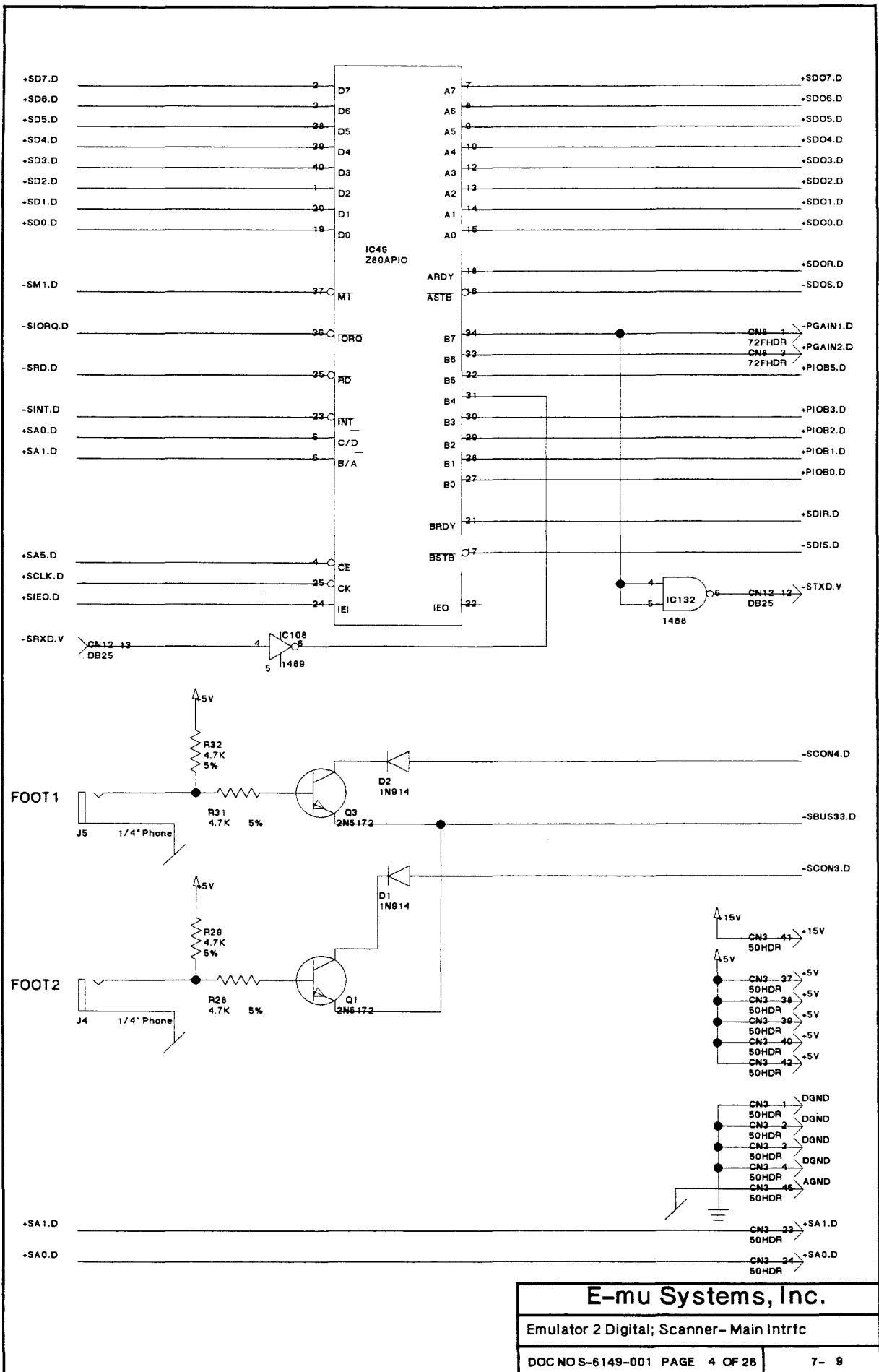
Emulator 2 Digital; Scanner-Ram & Rom

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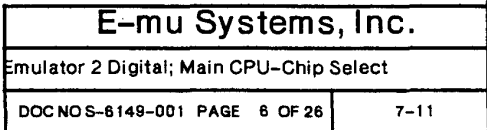
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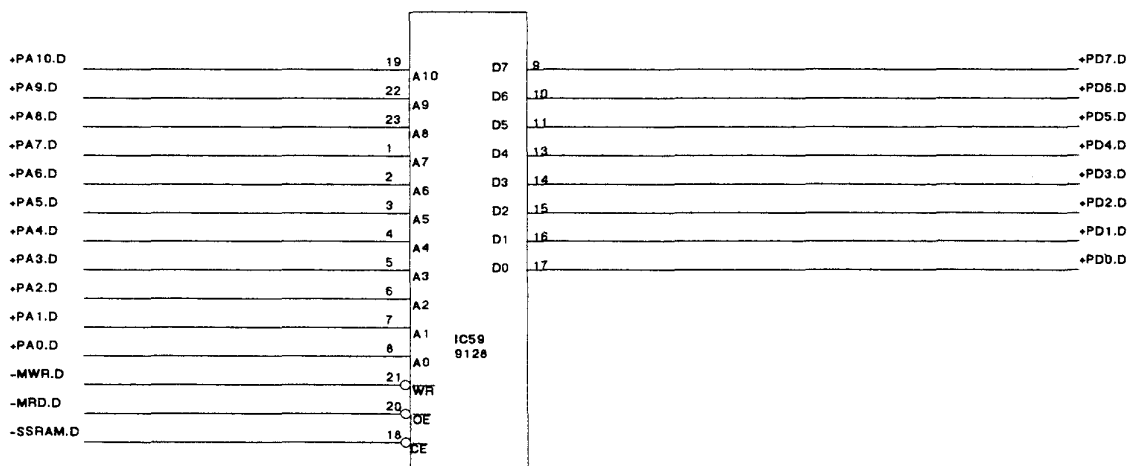
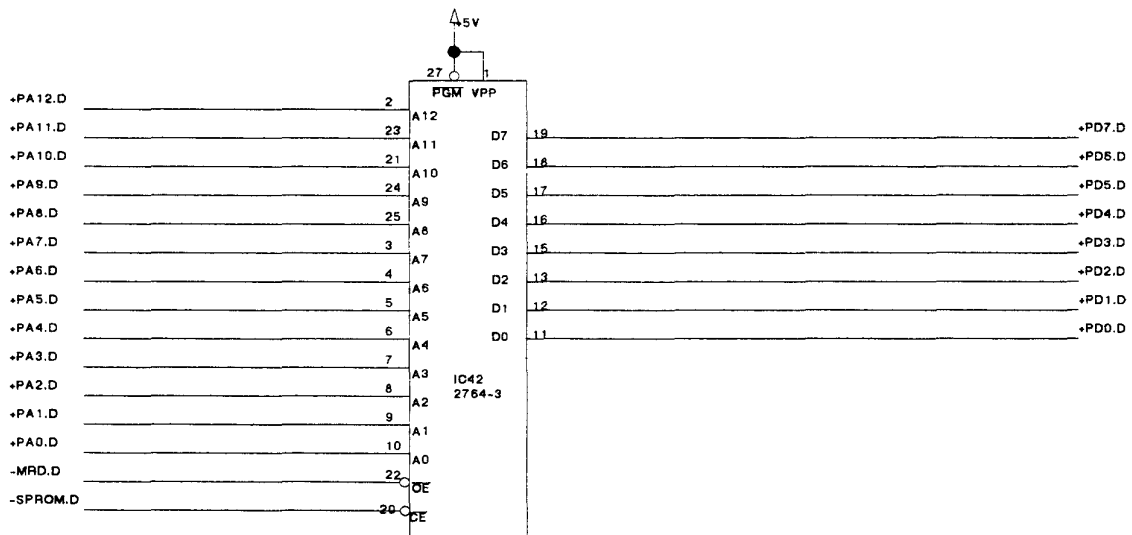
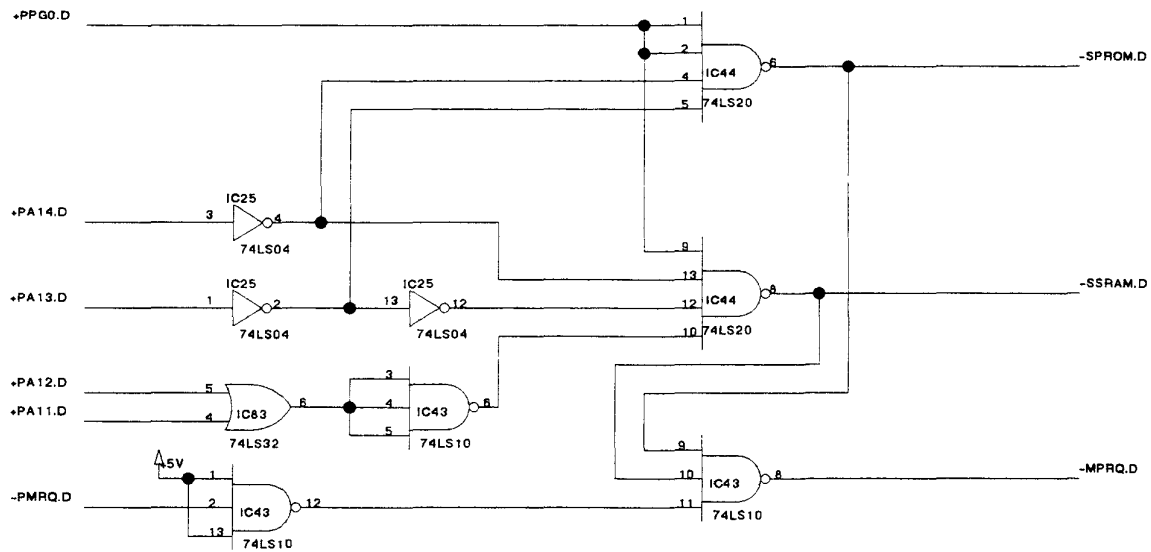






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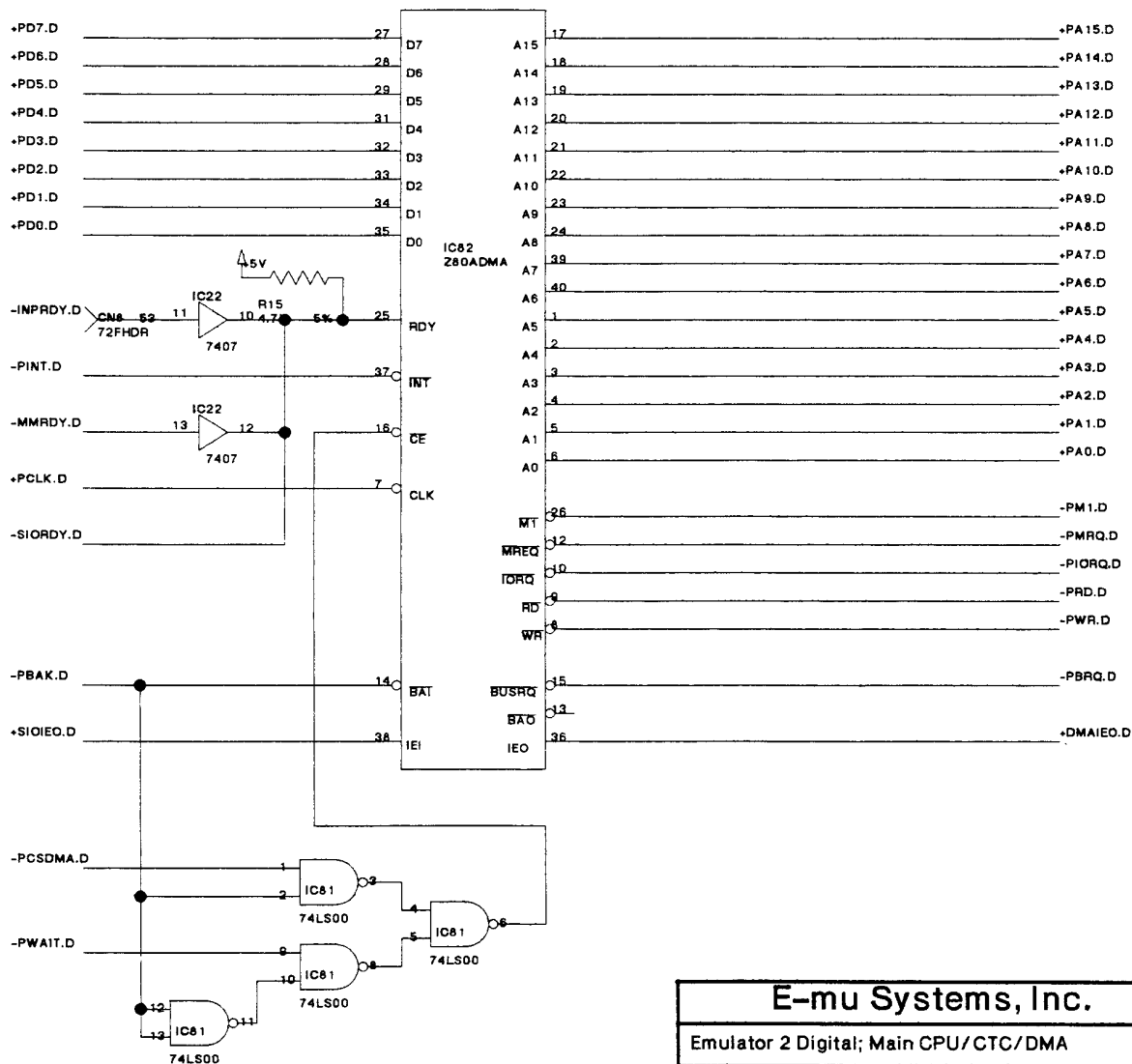
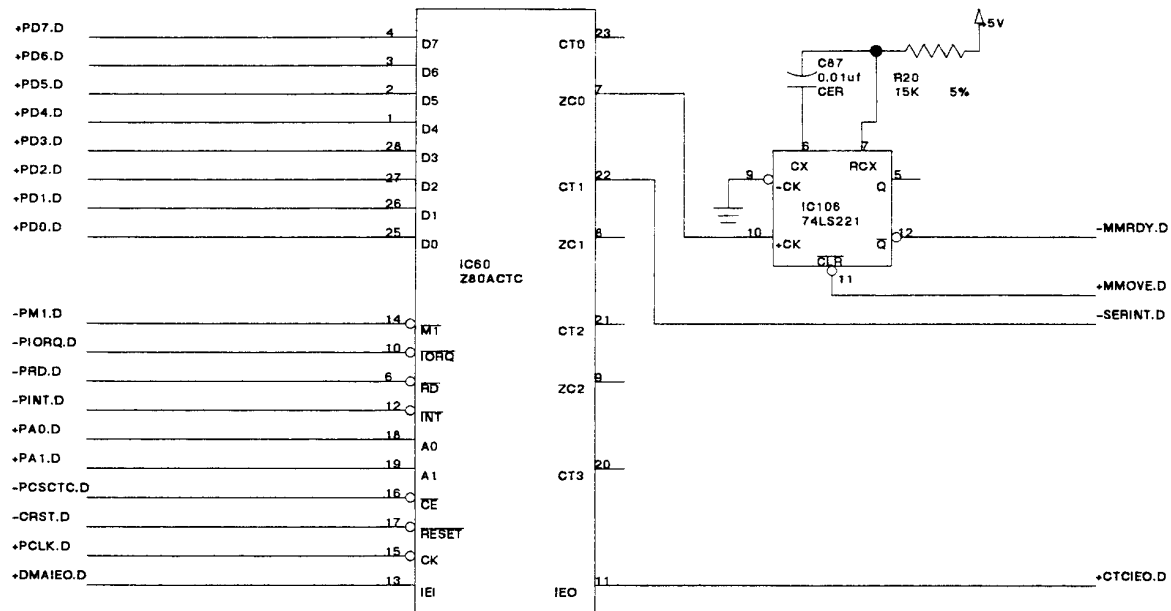


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Emulator 2 Digital; Main CPU -RAM&ROM

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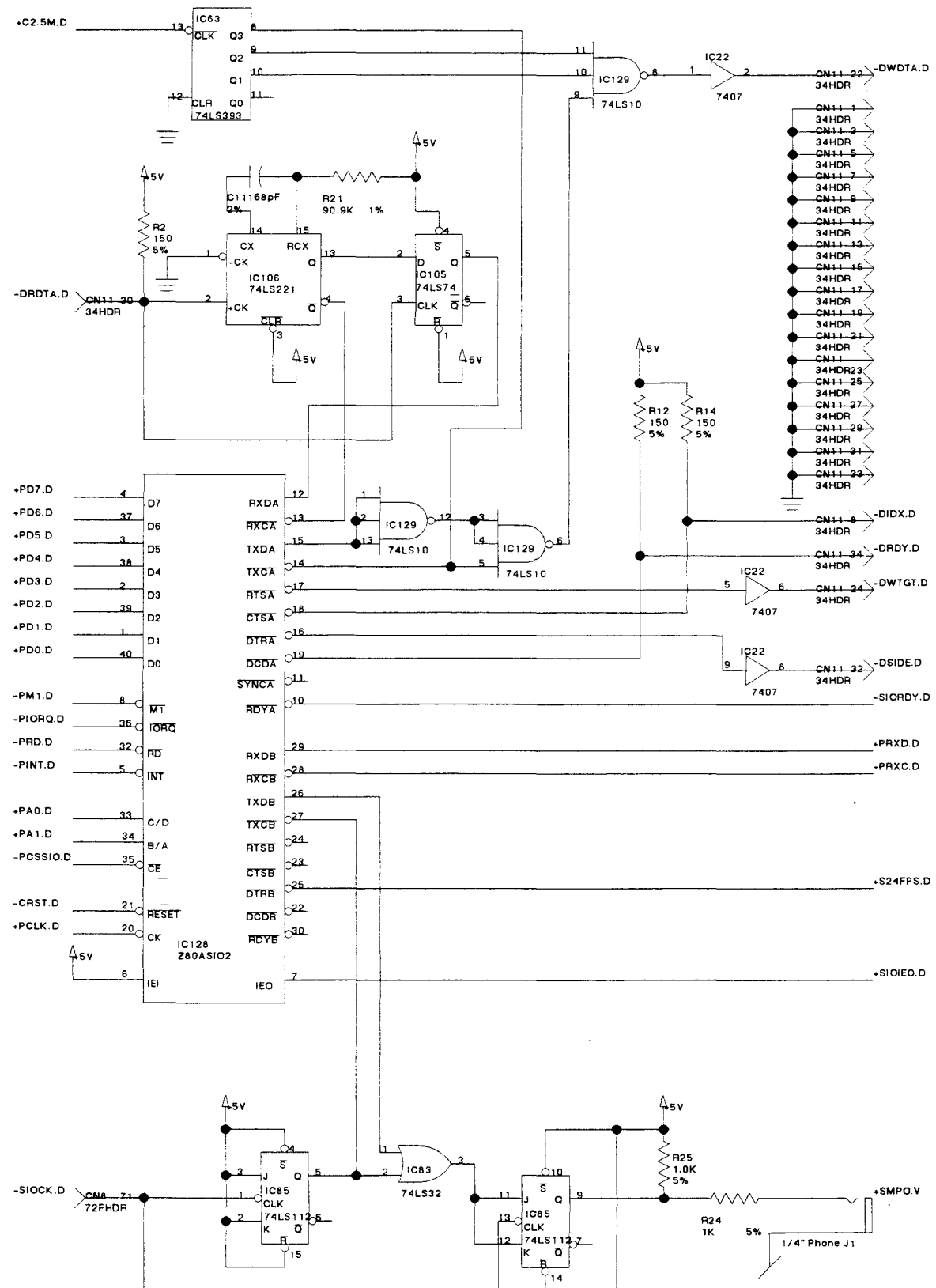
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Emulator 2 Digital; Main CPU/CTC/DMA

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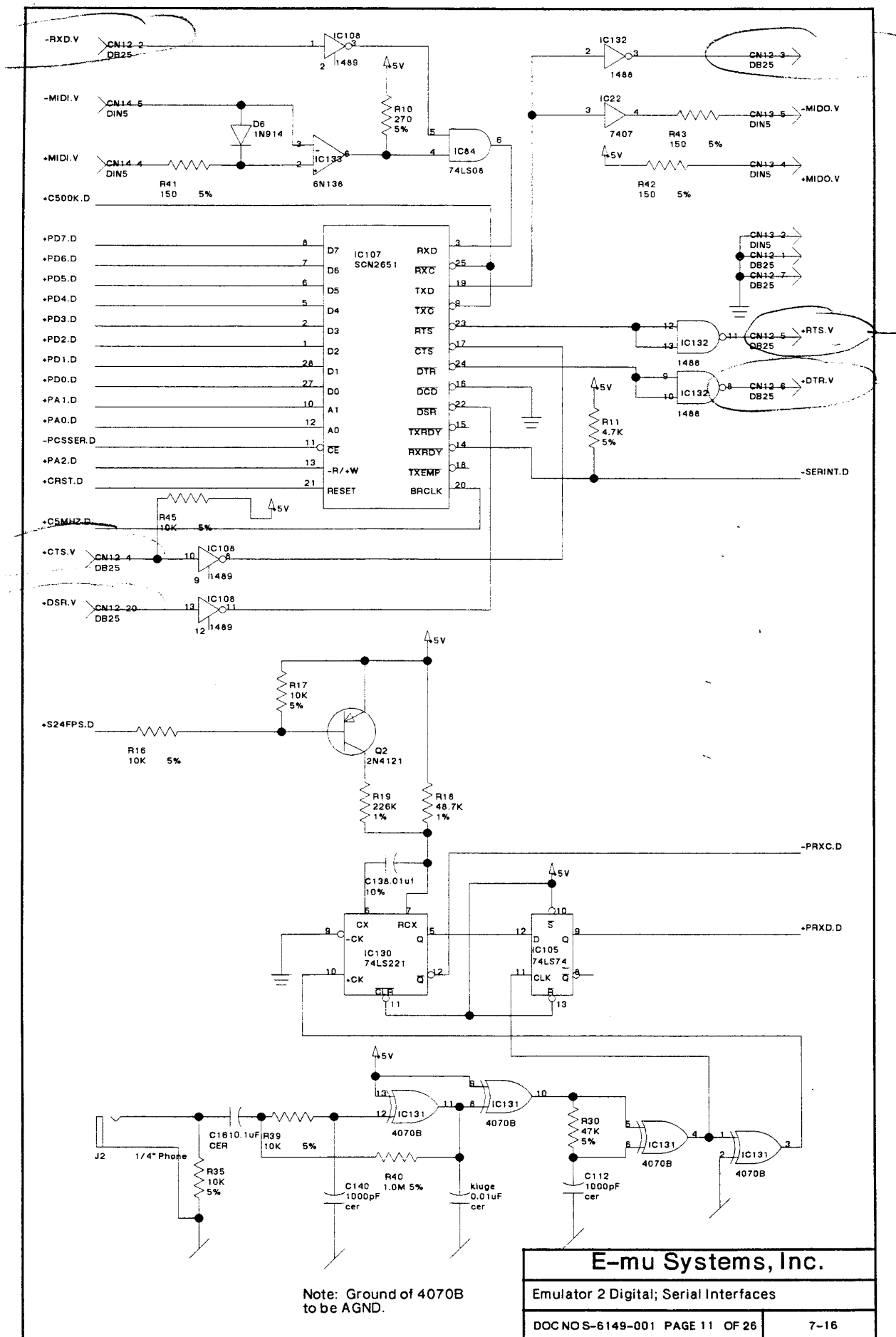
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Emulator 2 Digital; Disk Interface

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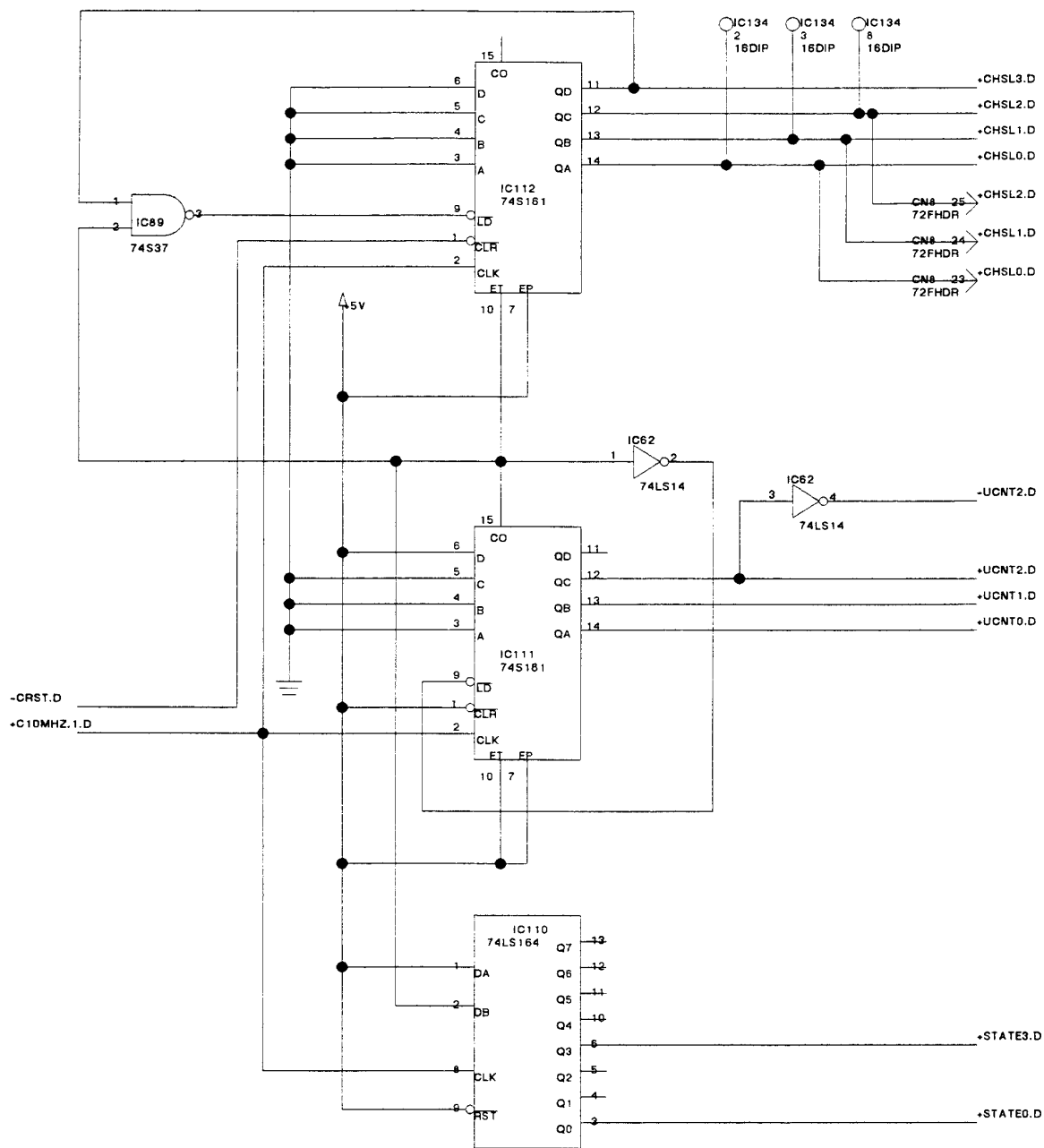


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Emulator 2 Digital; Serial Interfaces

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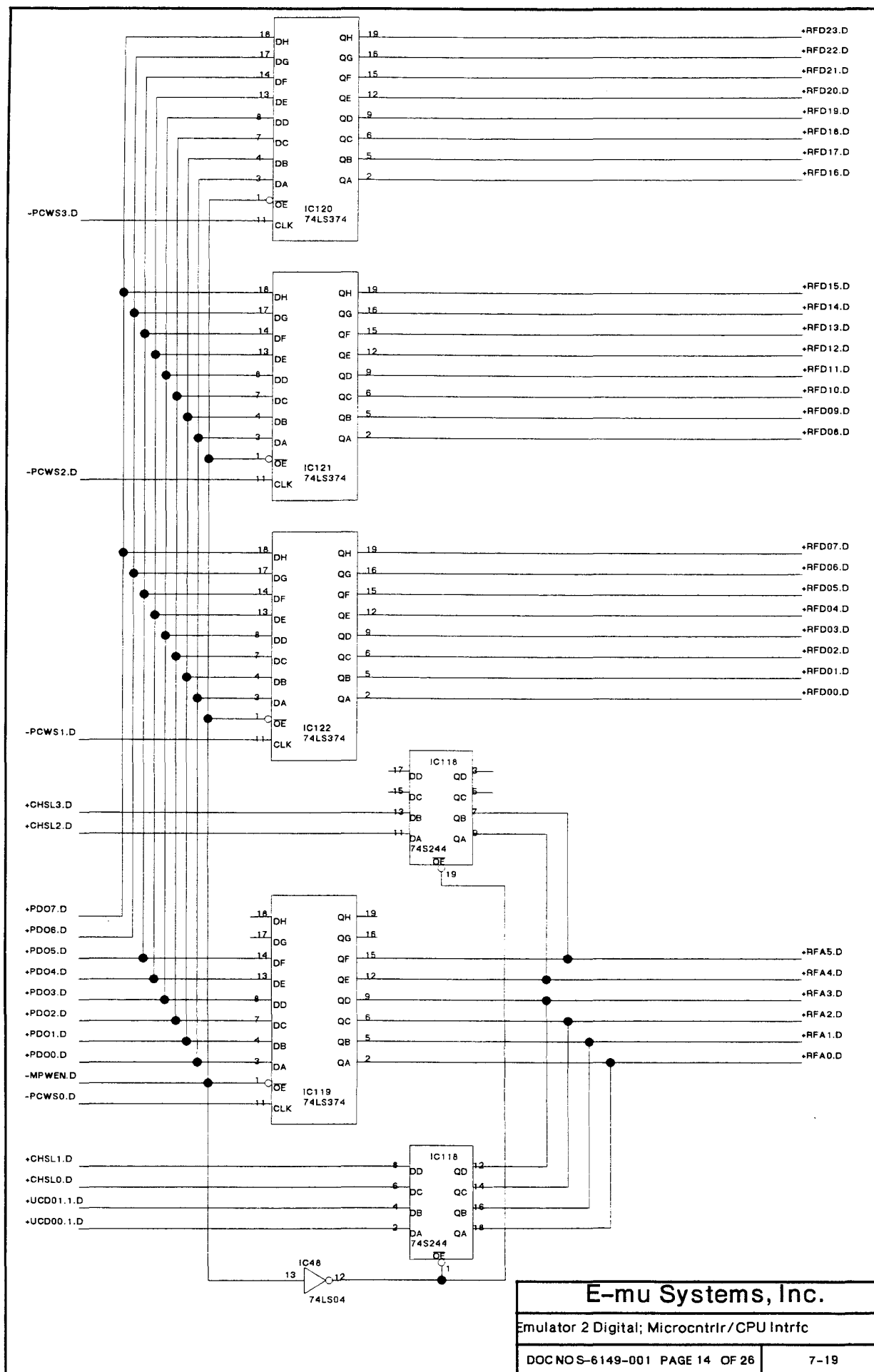
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Emulator 2 Digital; Microcntrlr State Ctr

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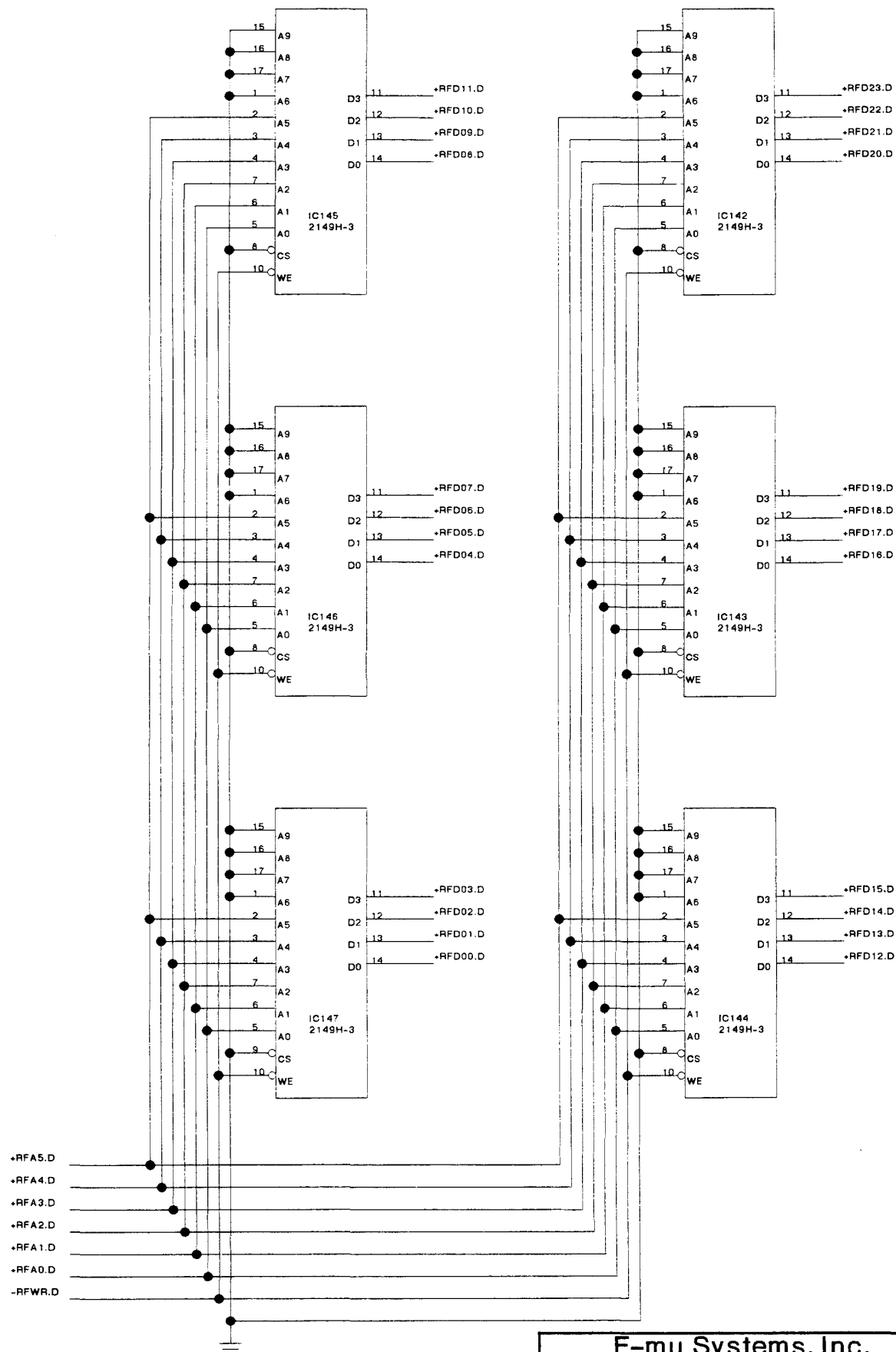


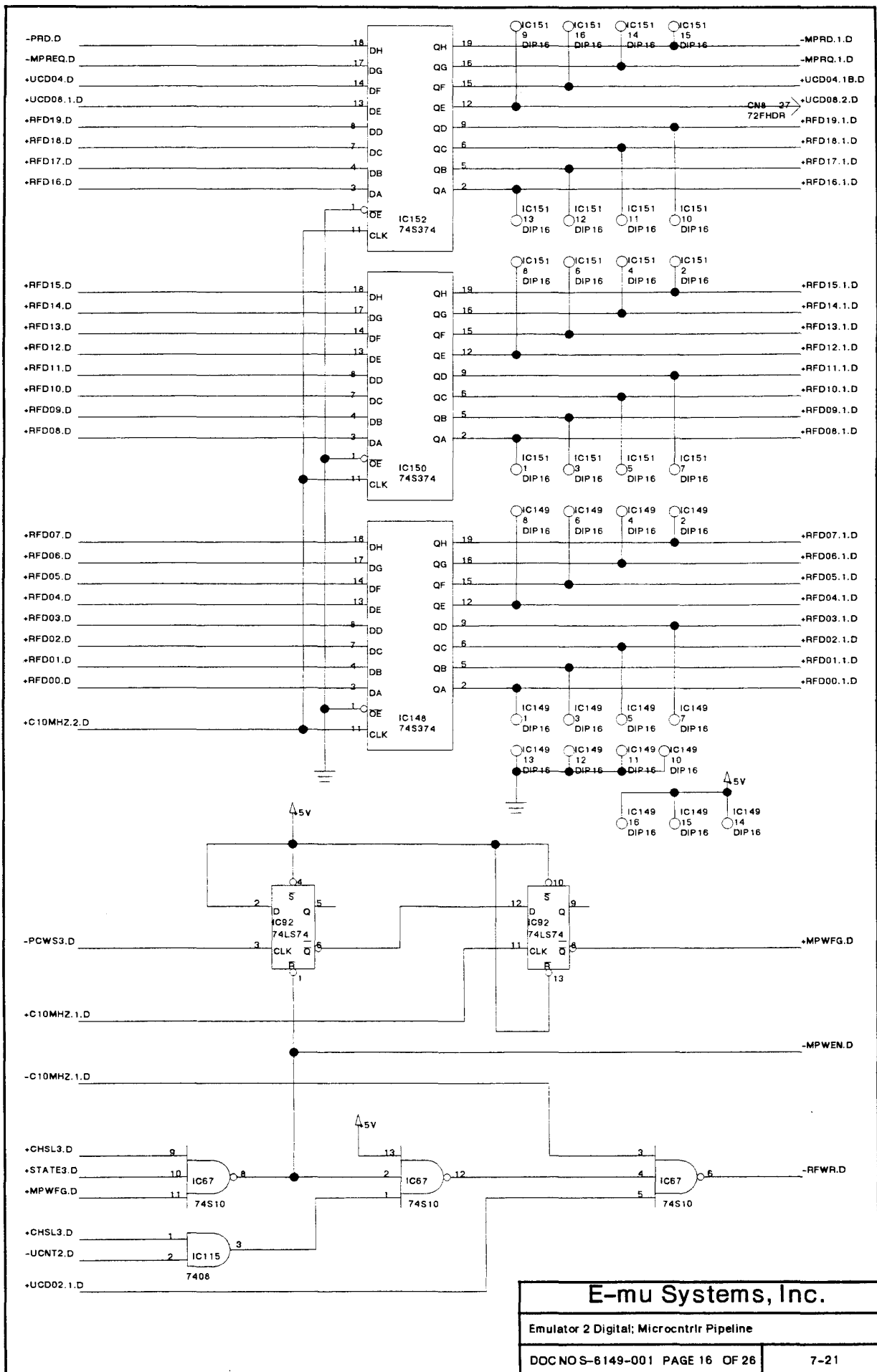
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Emulator 2 Digital; Microcncntr/CPU Intrfc

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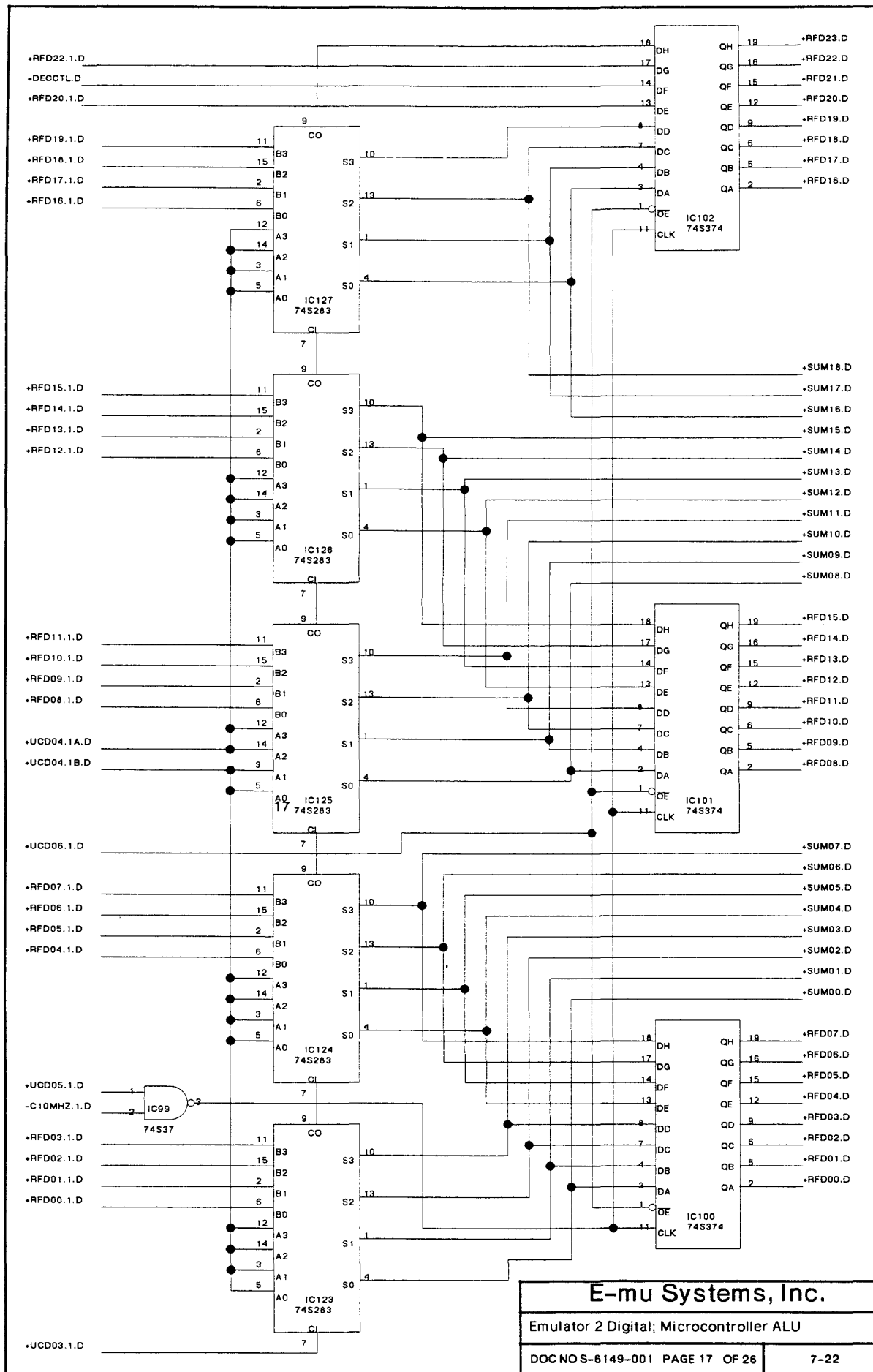


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Emulator 2 Digital; Microcntrlr Pipeline

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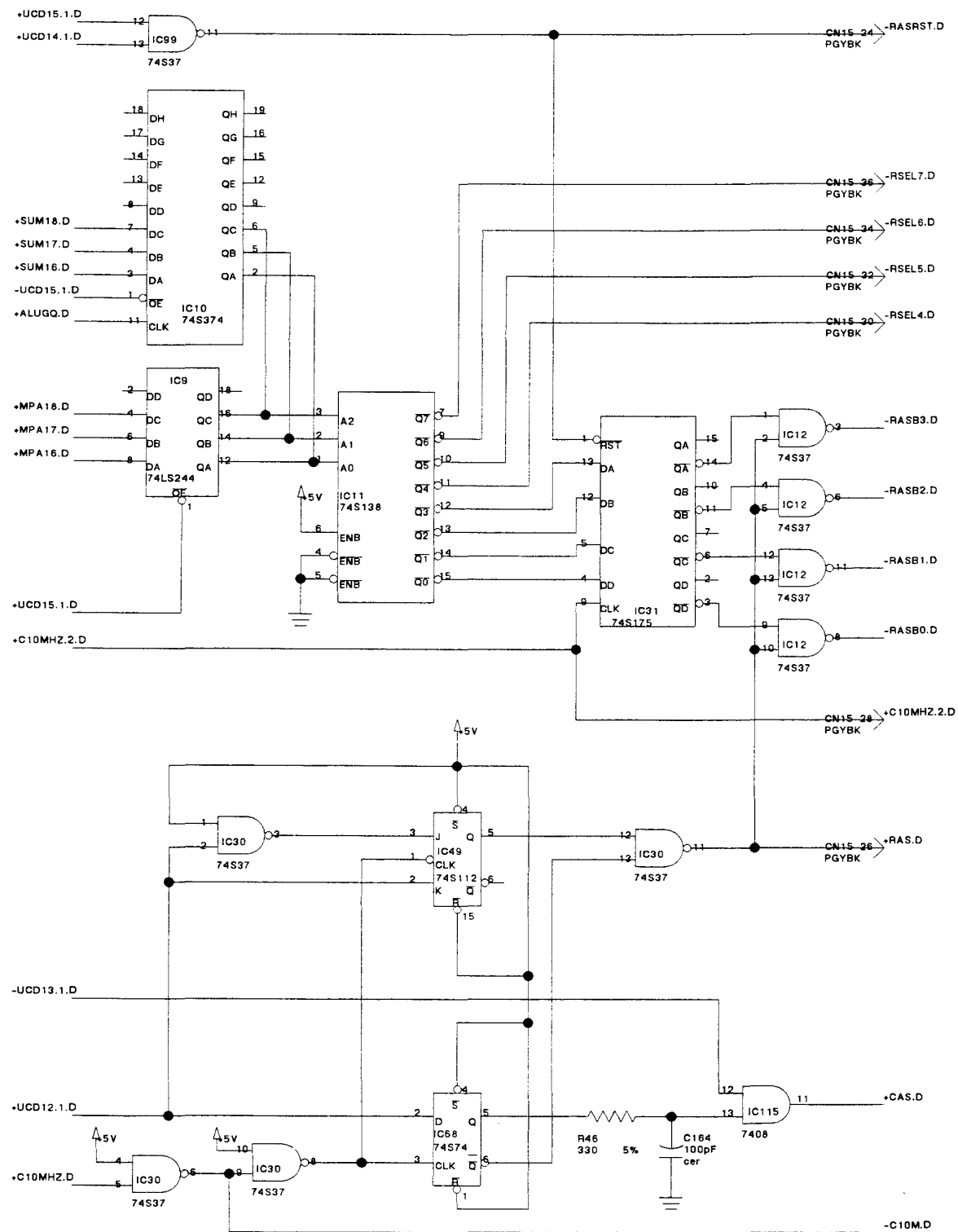


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Emulator 2 Digital; Microcontroller ALU

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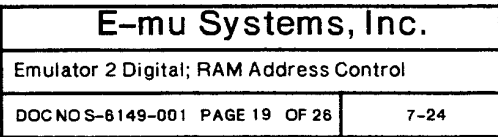
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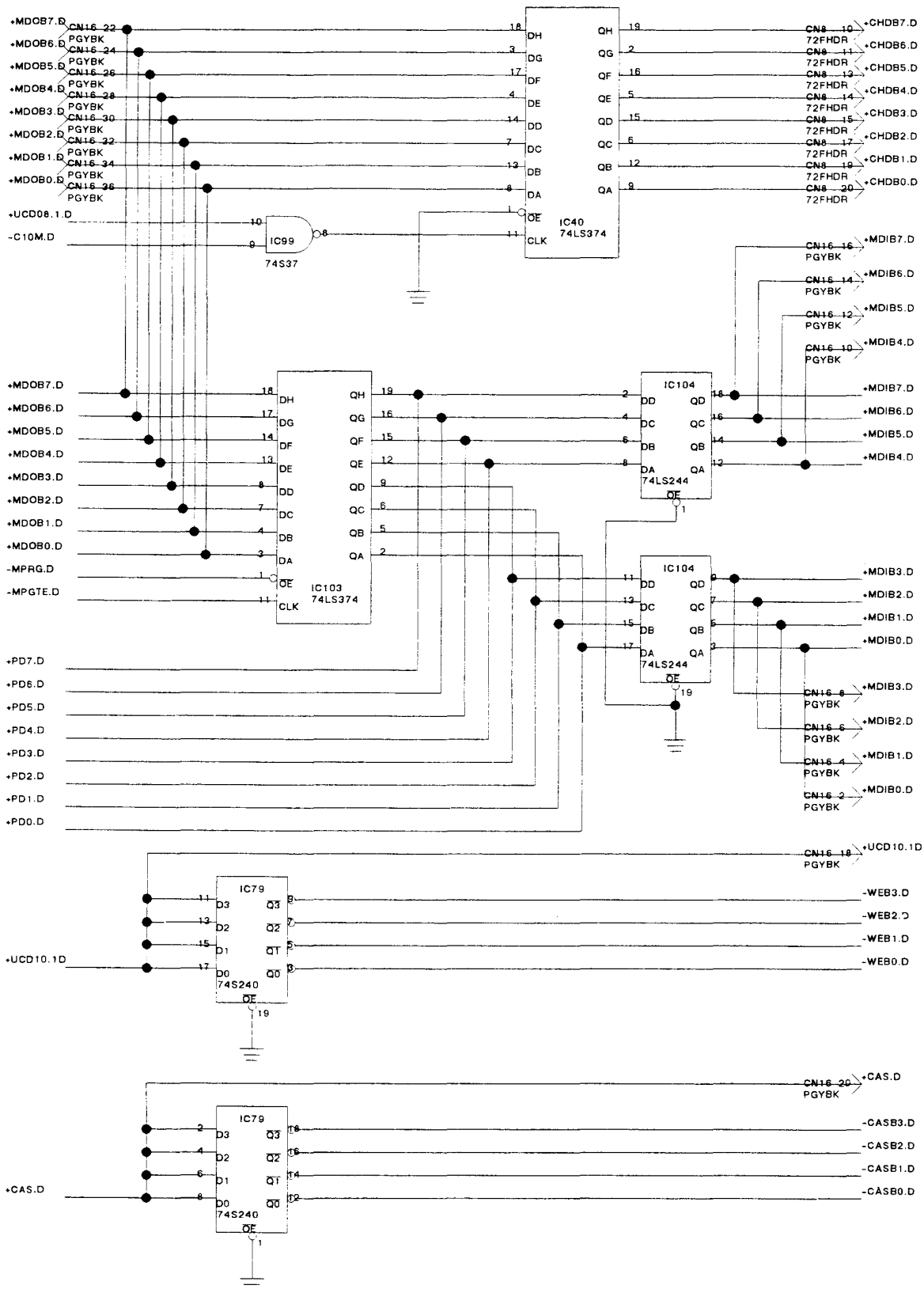
Emulator 2 Digital; RAM Timing

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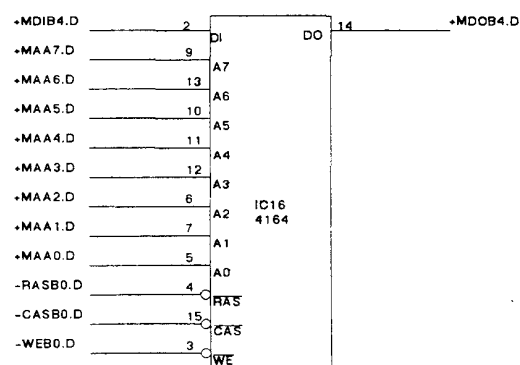
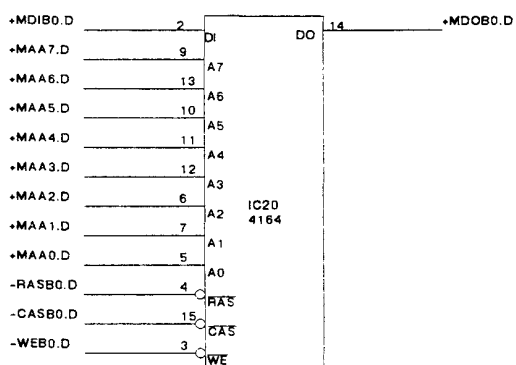
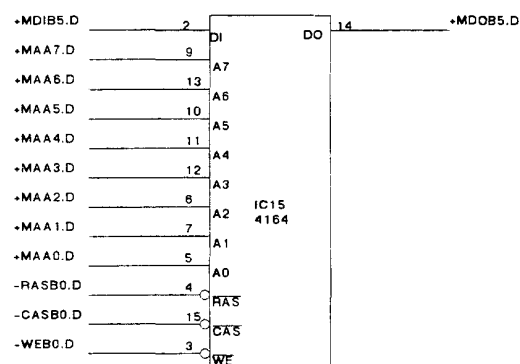
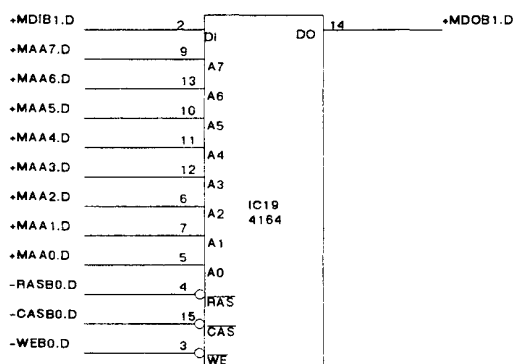
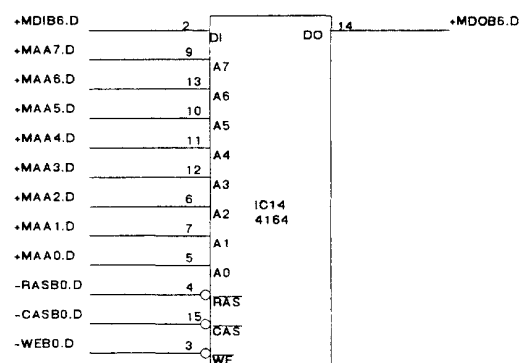
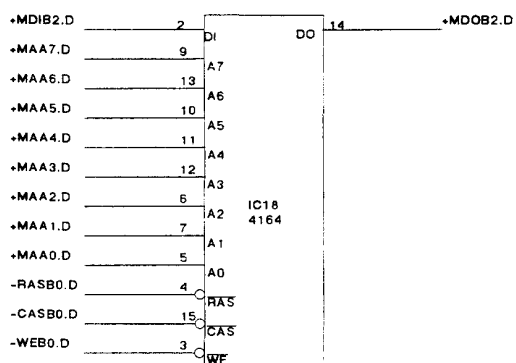
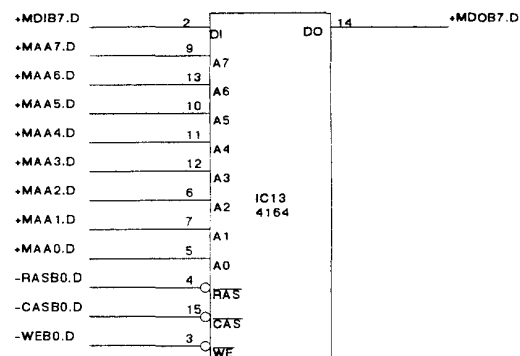
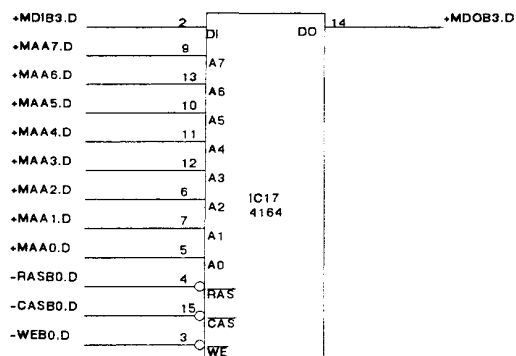


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Emulator 2 Digital; RAM Buffering

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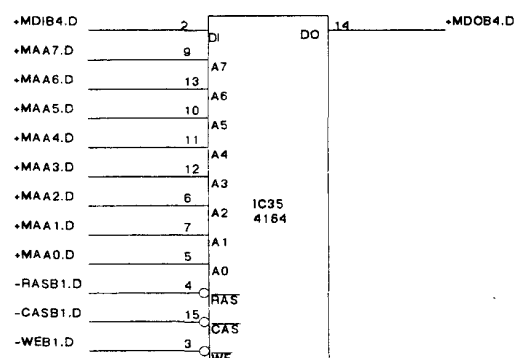
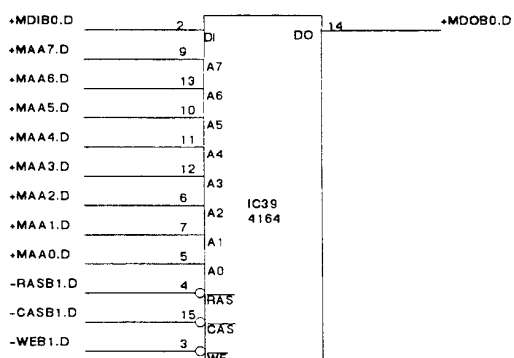
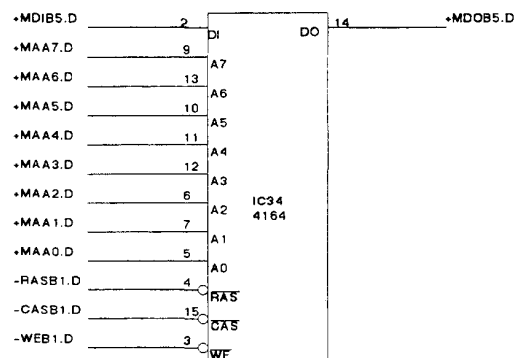
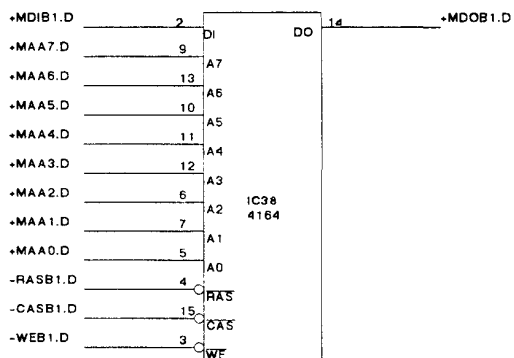
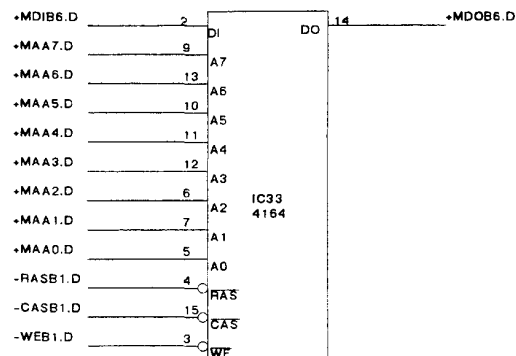
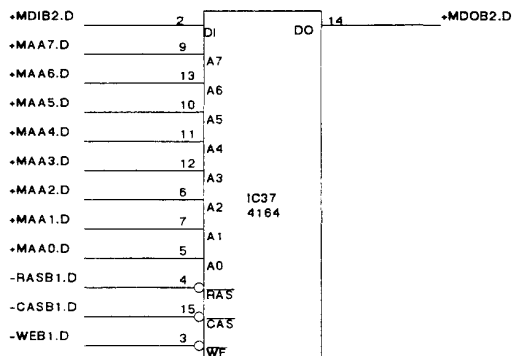
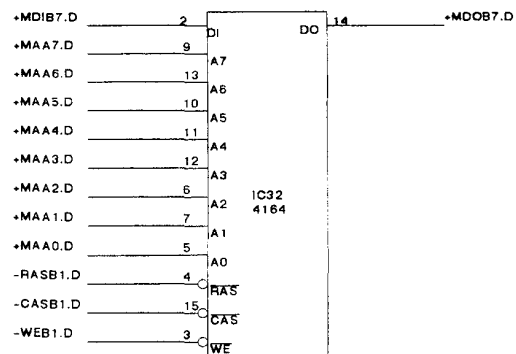
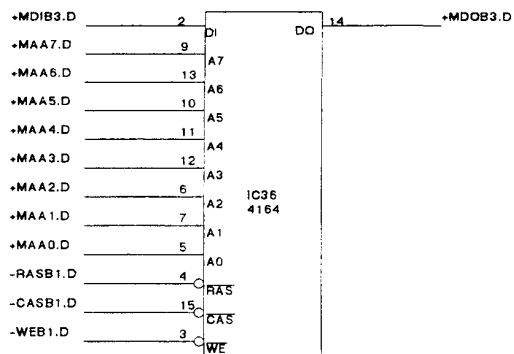


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Emulator 2 Digital; Dynamic RAM

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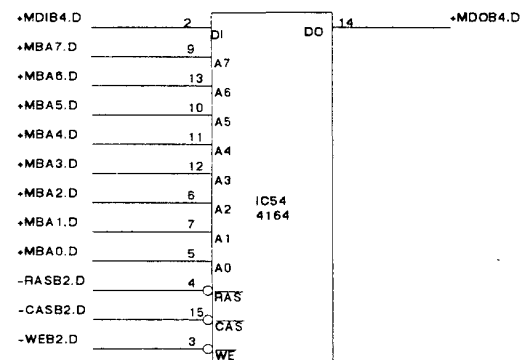
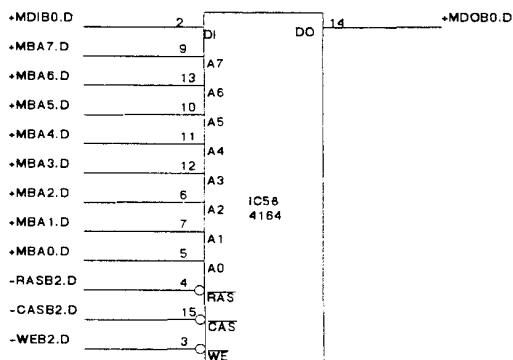
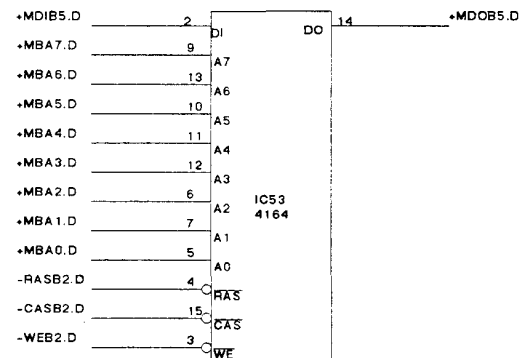
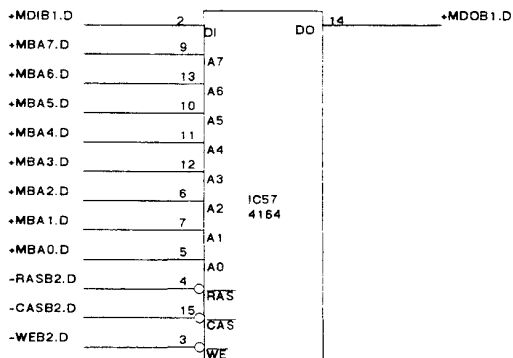
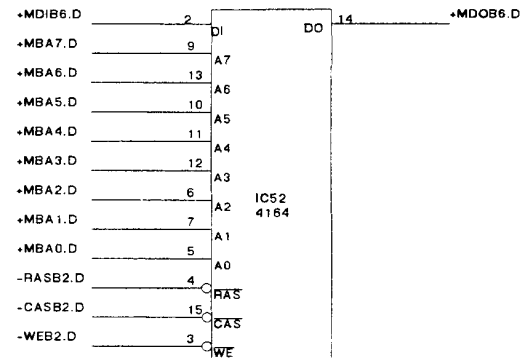
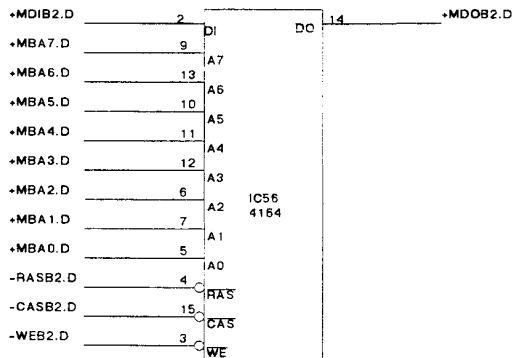
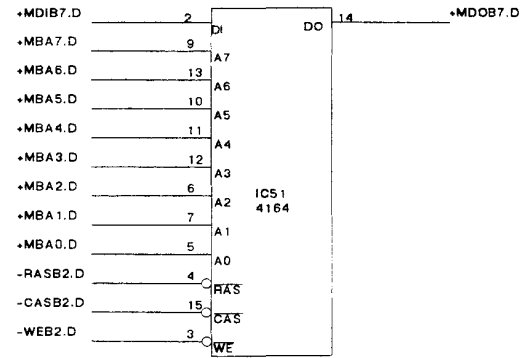
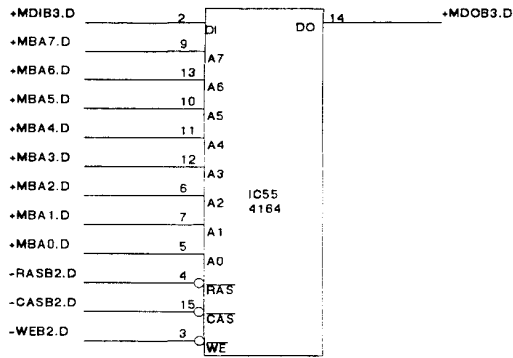


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Emulator 2 Digital; Dynamic RAM

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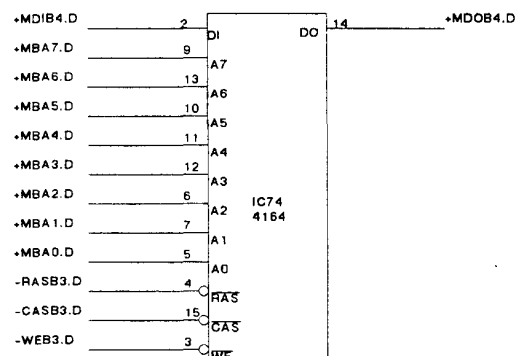
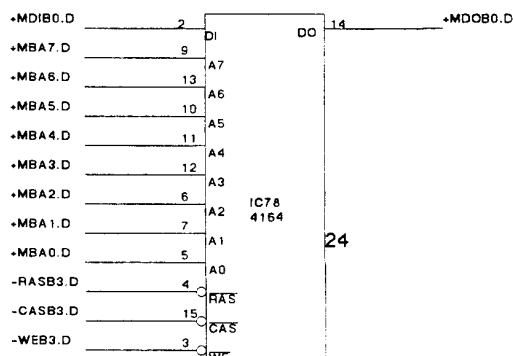
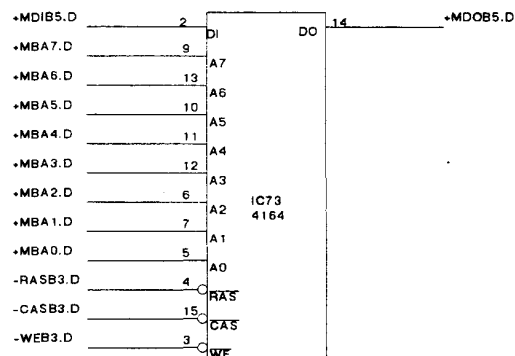
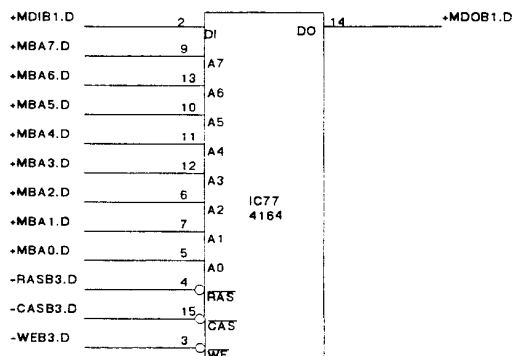
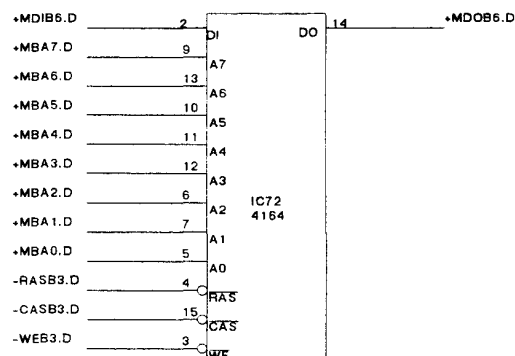
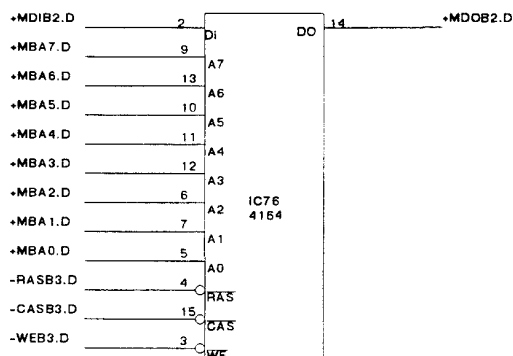
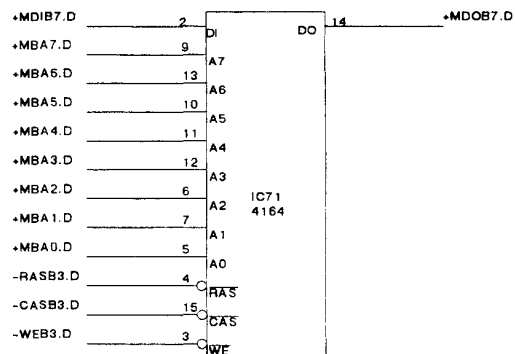
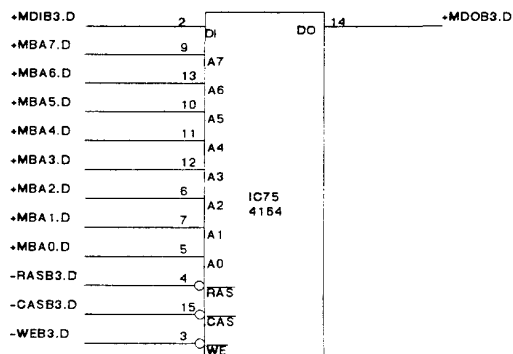


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Emulator 2 Digital; Dynamic RAM

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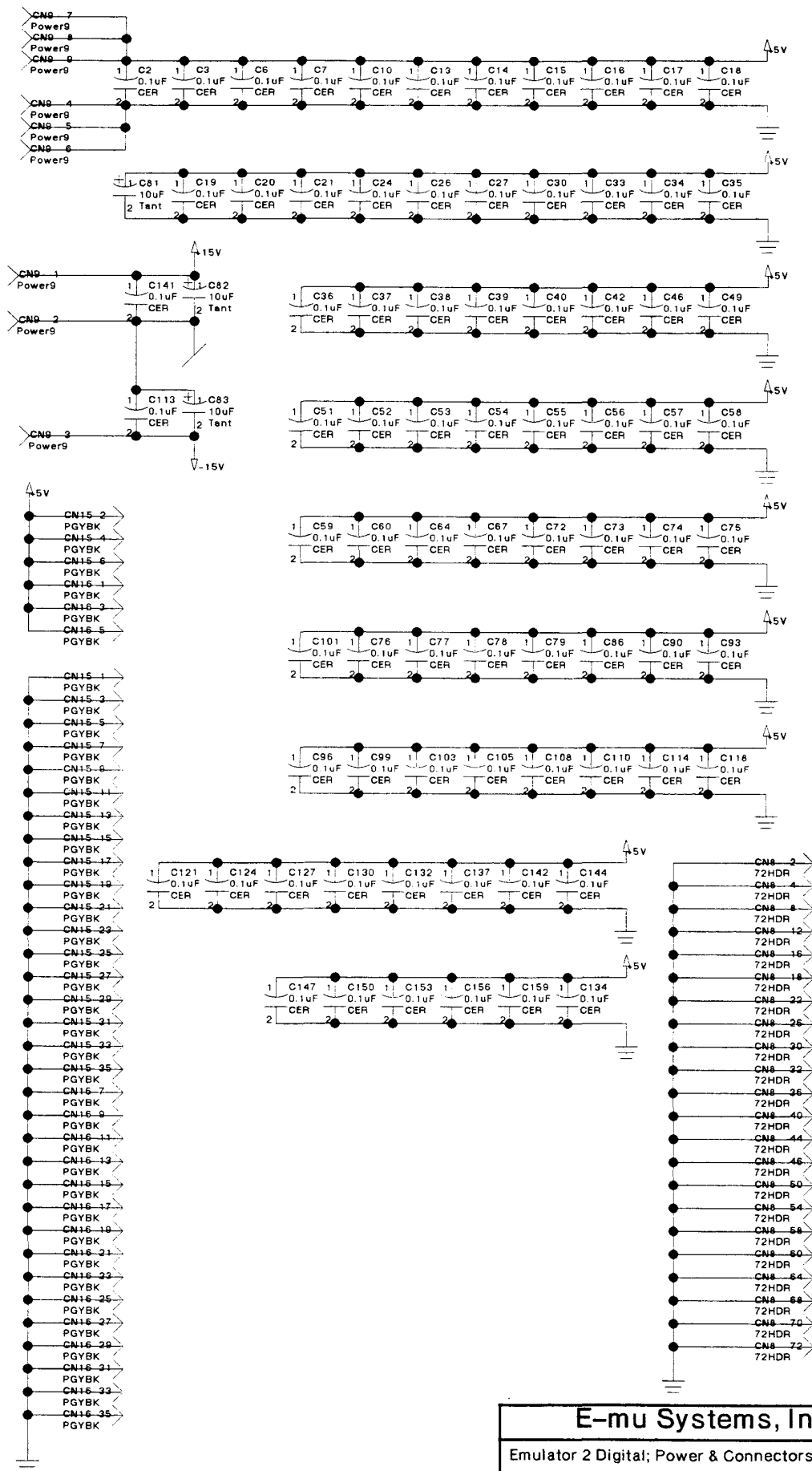
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Emulator 2 Digital; Dynamic RAM

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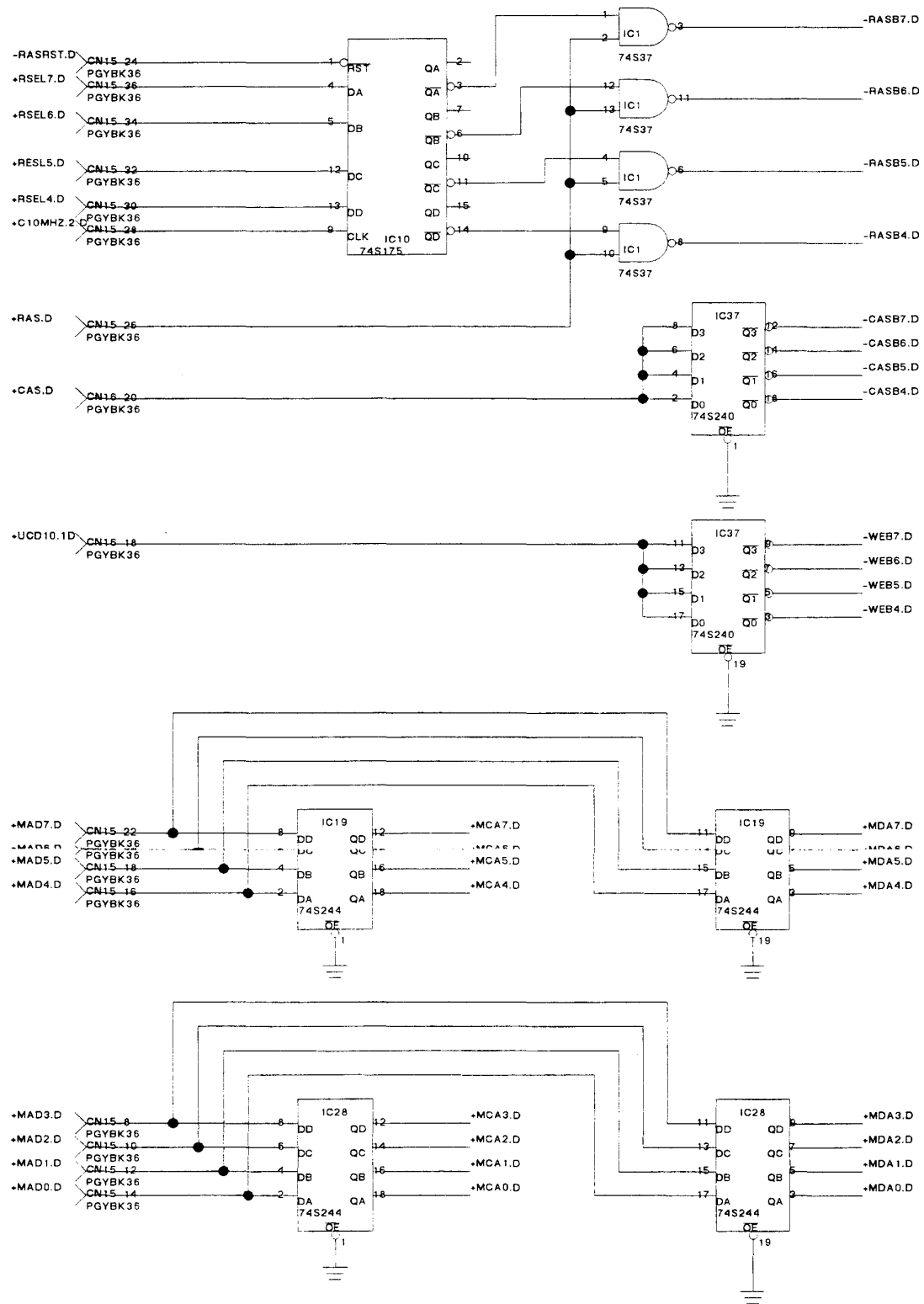
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Emulator 2 Digital; Power & Connectors

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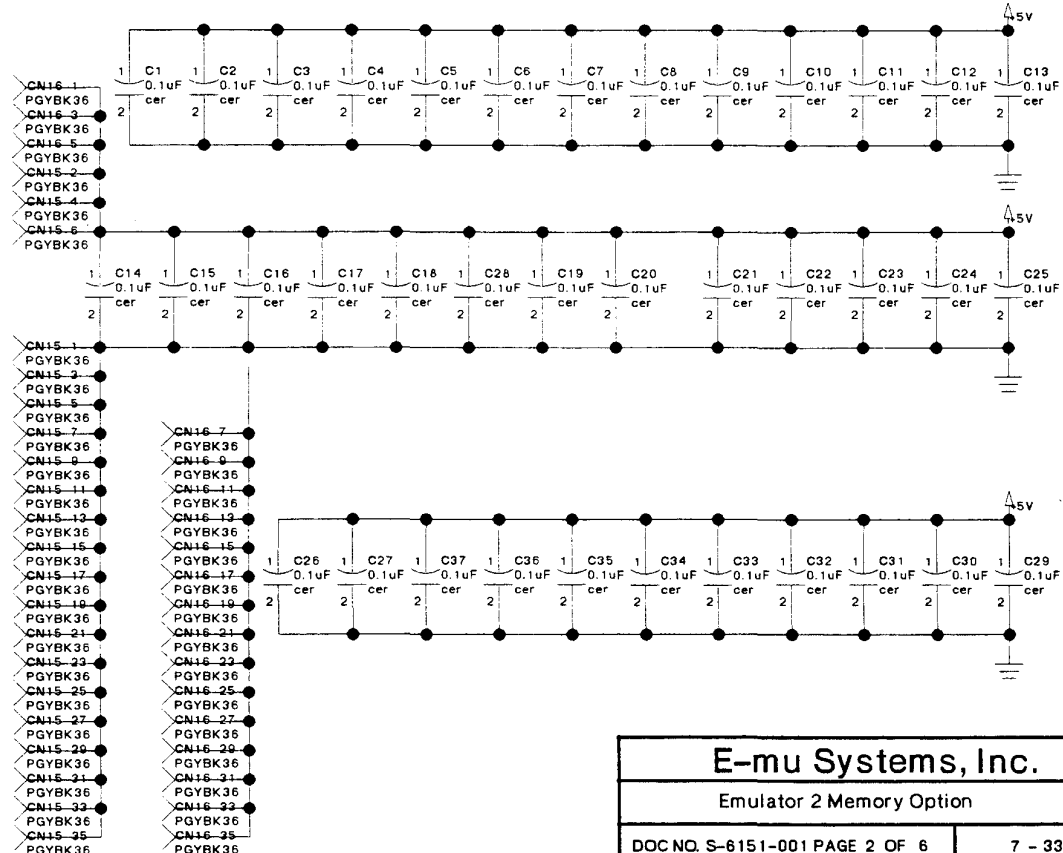
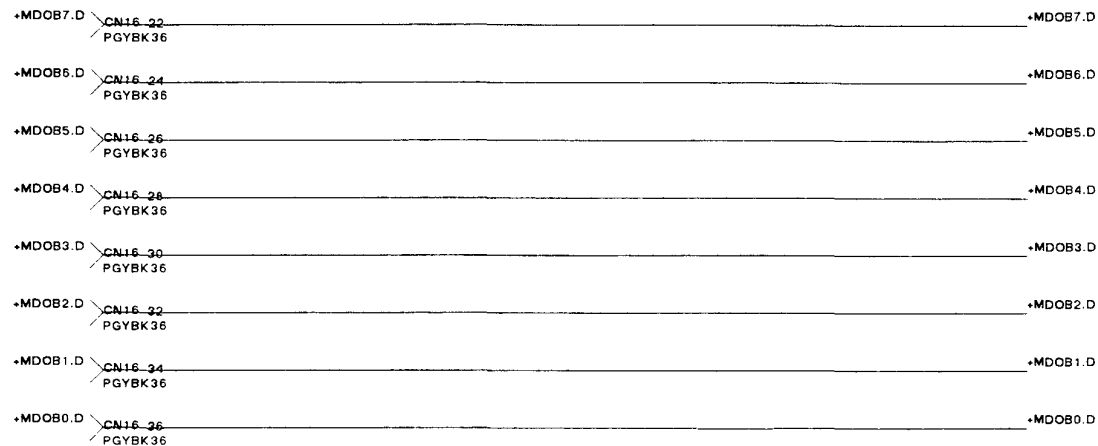
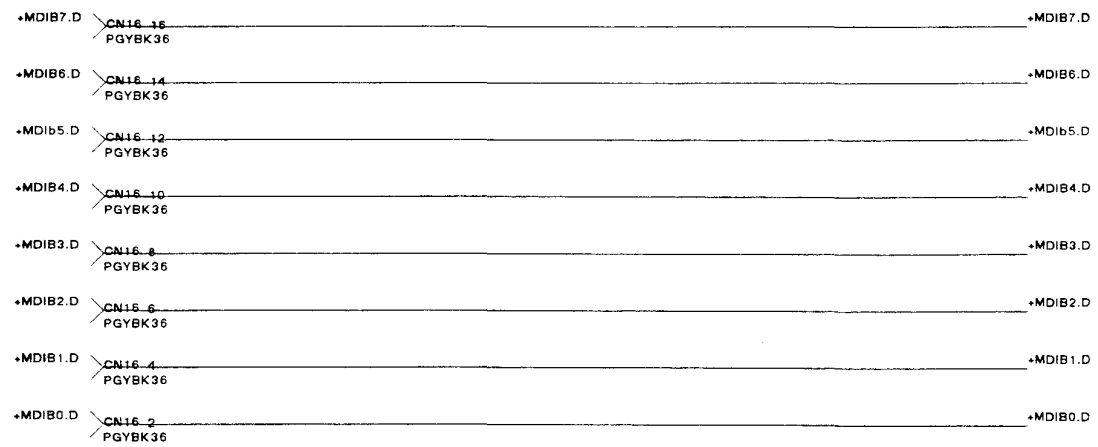


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Emulator 2 Memory Option

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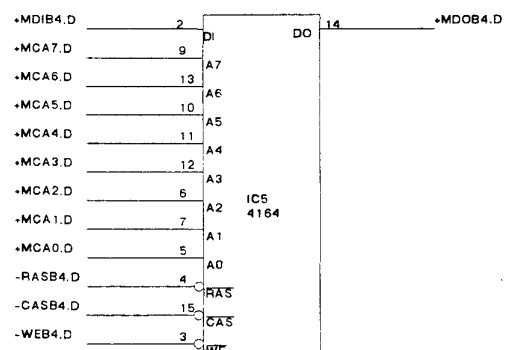
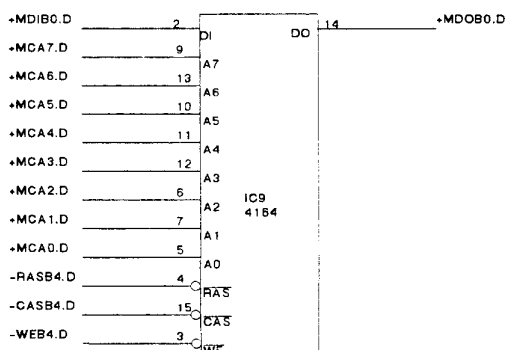
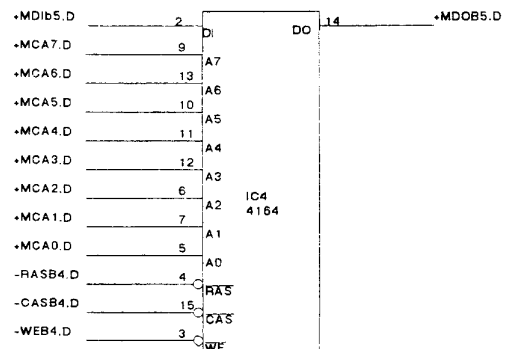
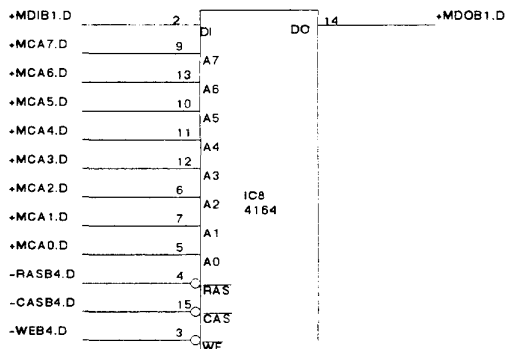
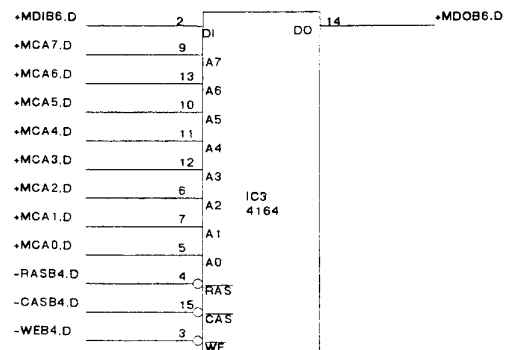
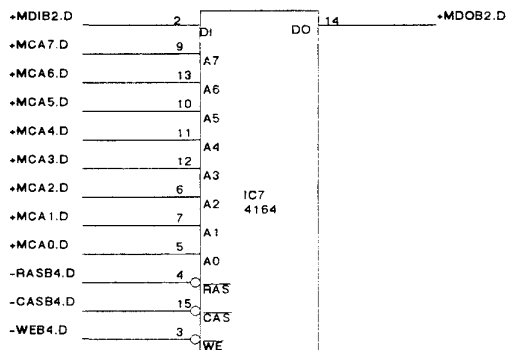
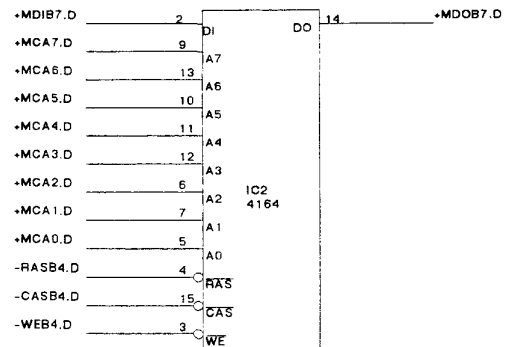
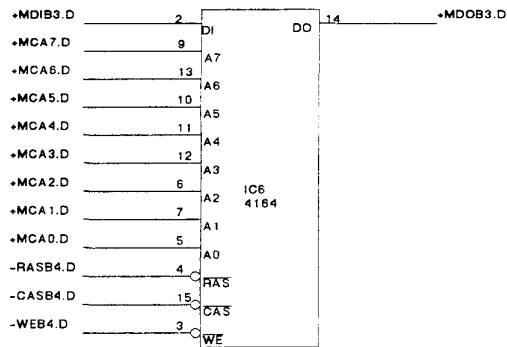


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### Emulator 2 Memory Option

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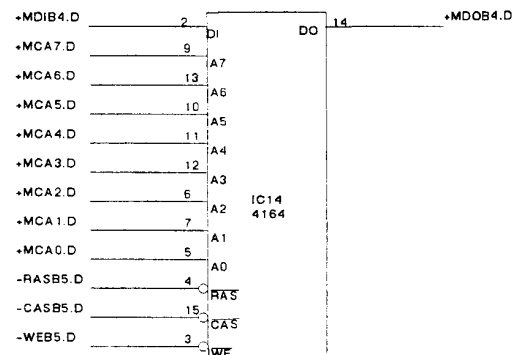
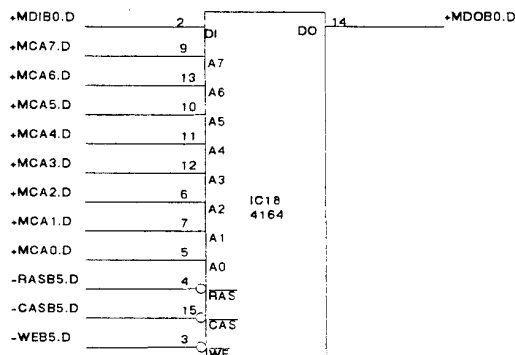
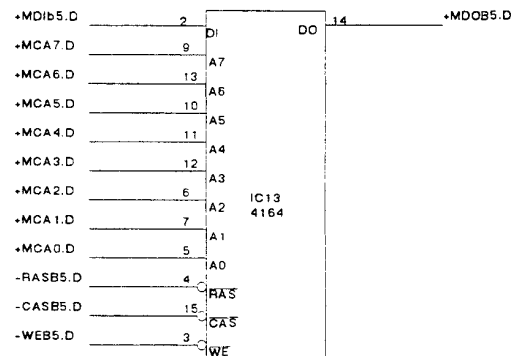
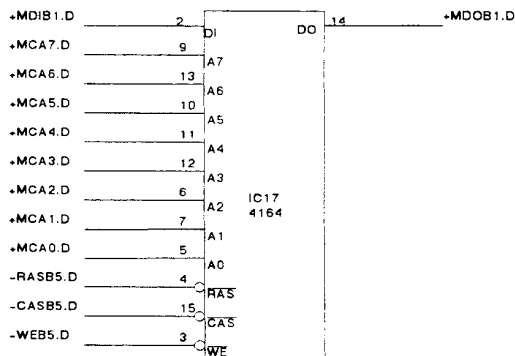
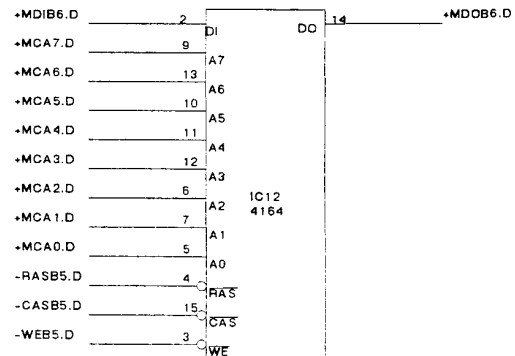
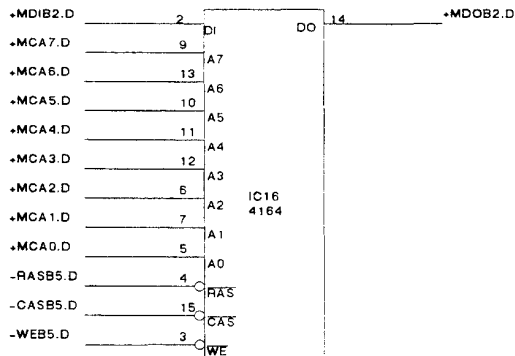
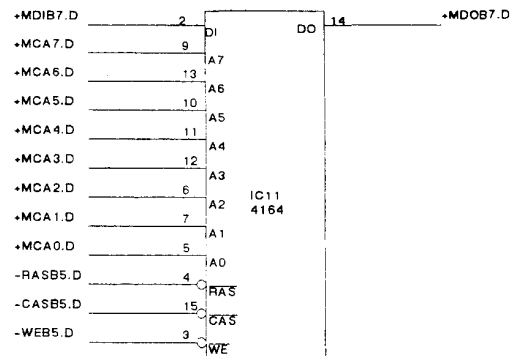
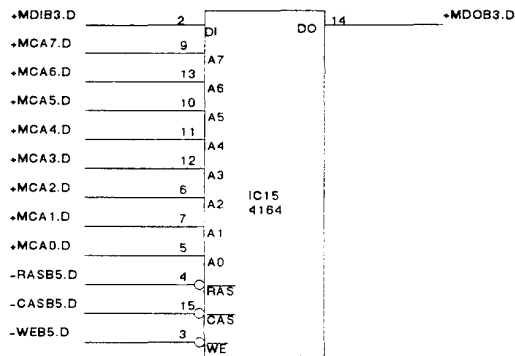


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Emulator 2 Memory Option

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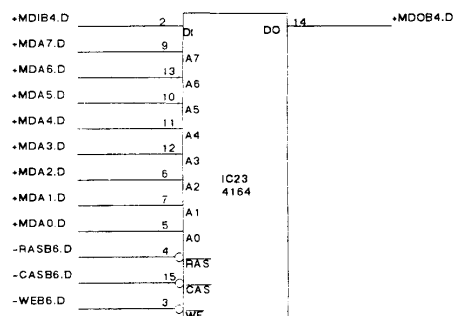
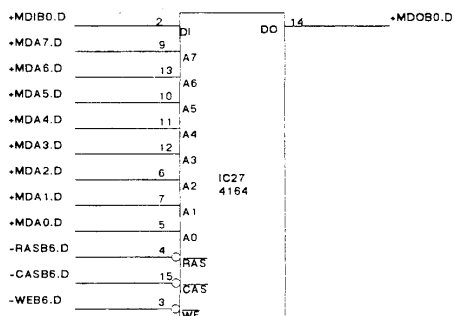
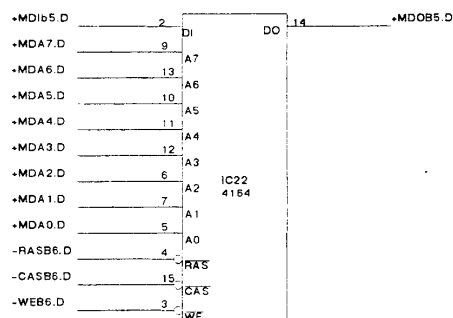
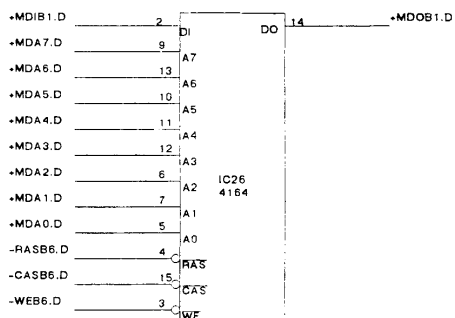
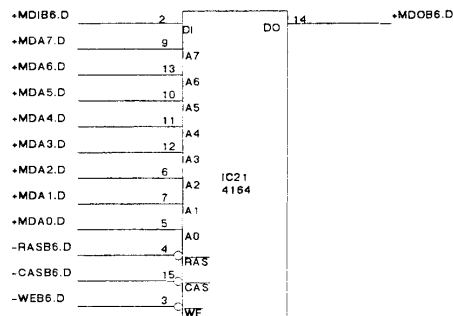
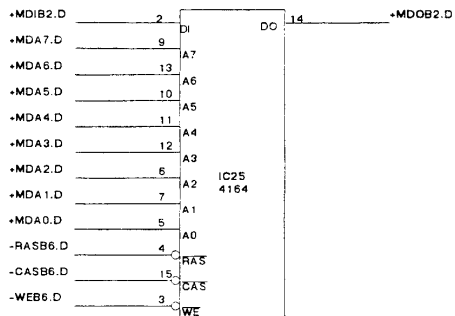
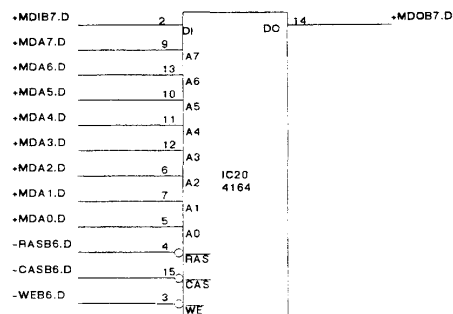
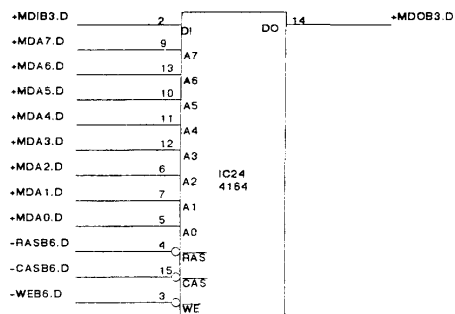


E-mu Systems, Inc.

Emulator 2 Memory Option

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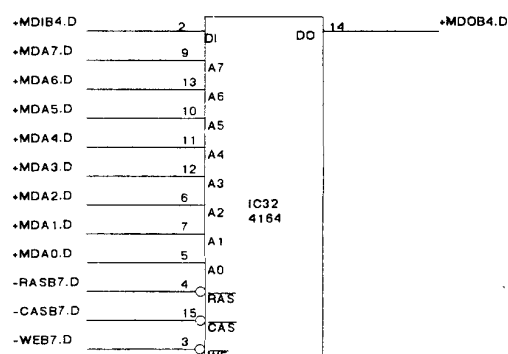
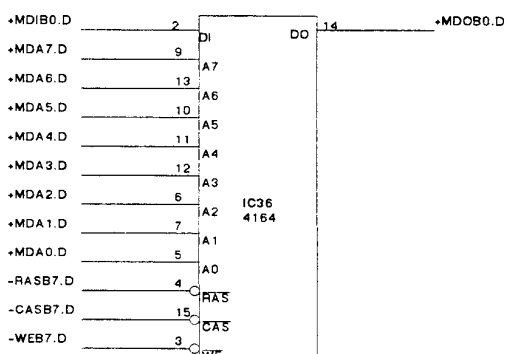
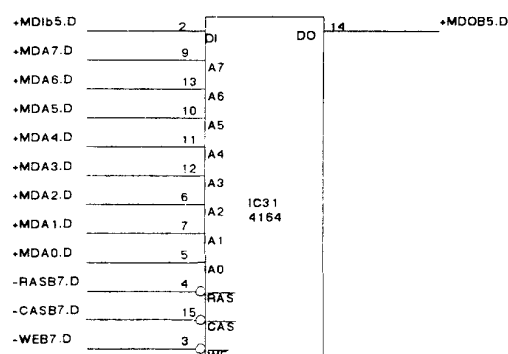
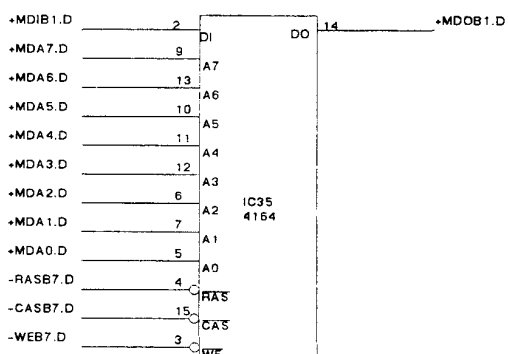
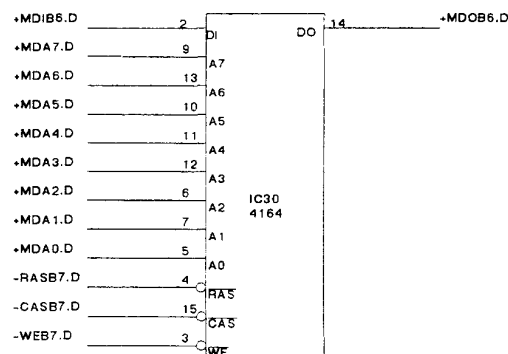
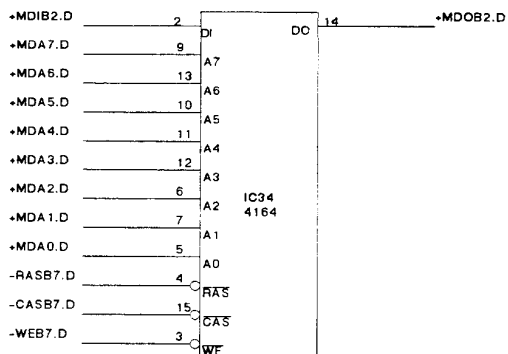
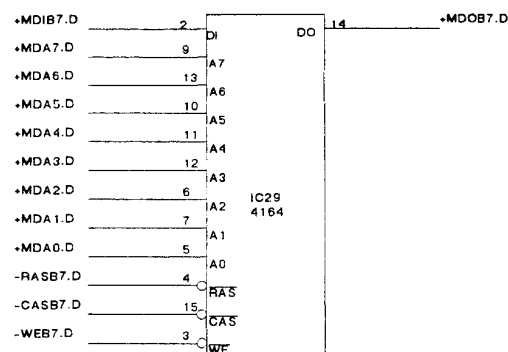
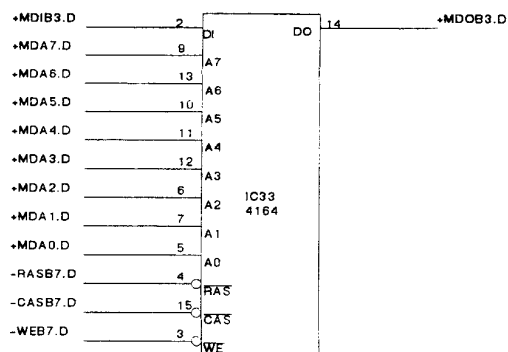


E-mu Systems, Inc.

Emulator 2 Memory Option

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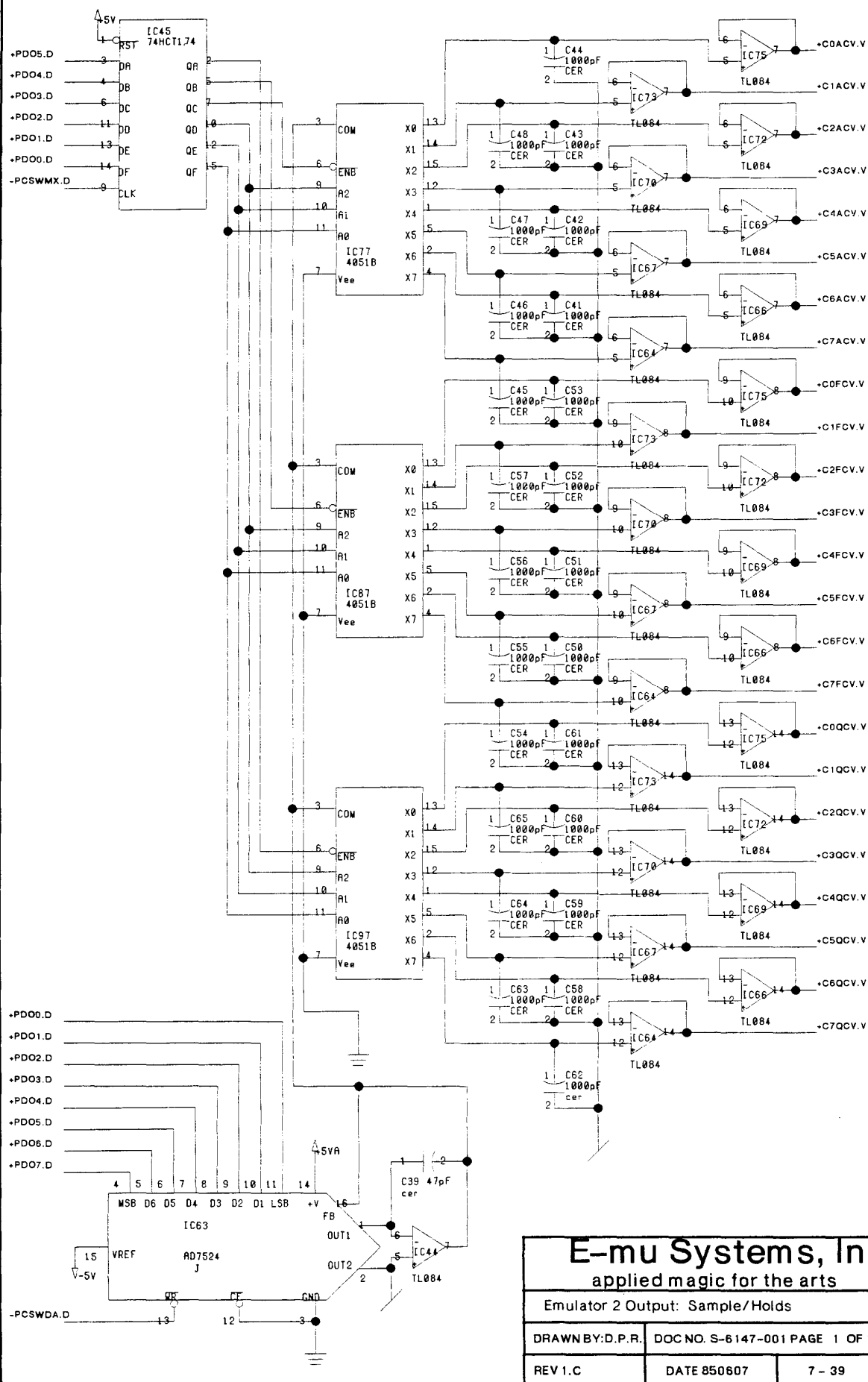


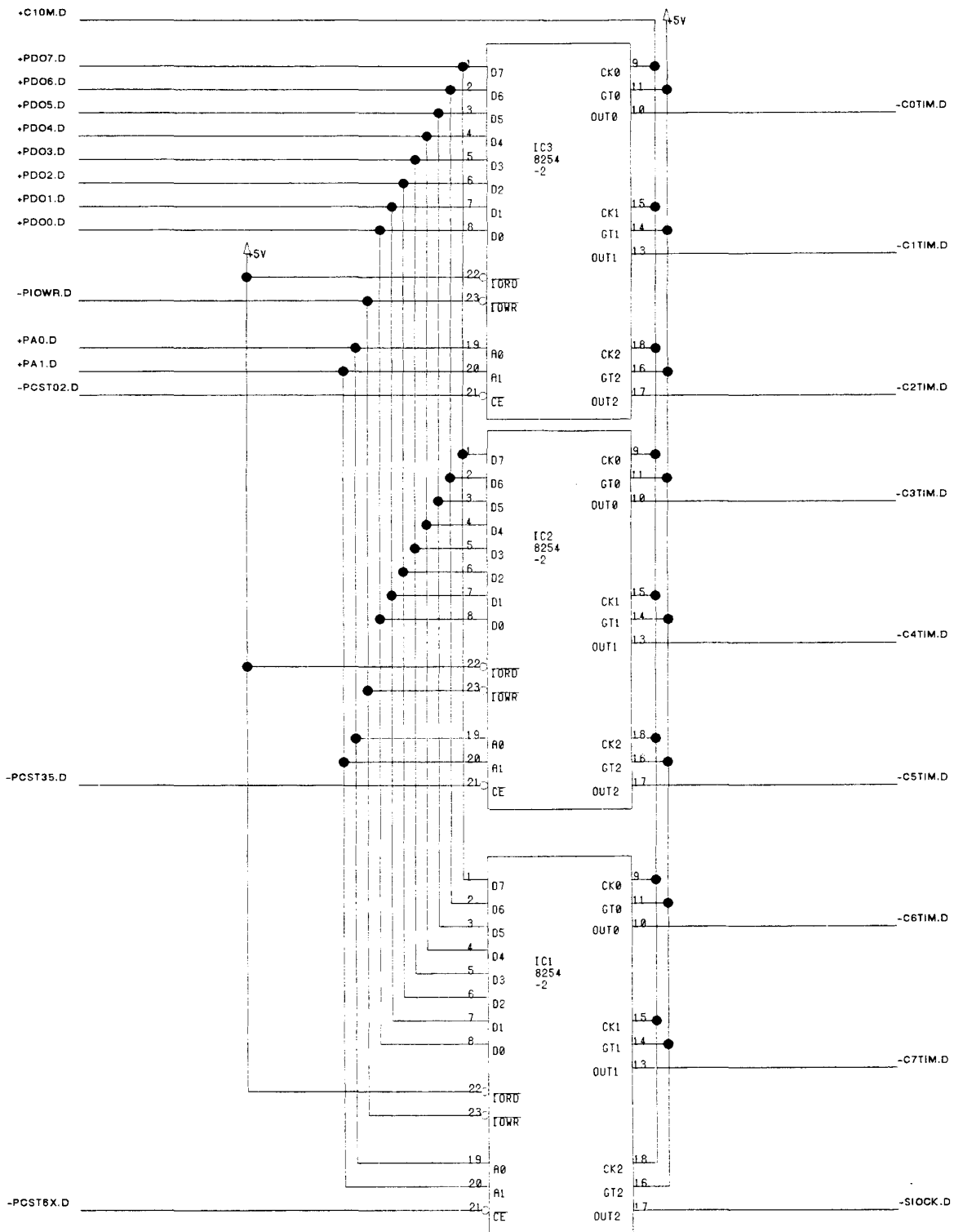
E-mu Systems, Inc.

Emulator 2 Memory Option

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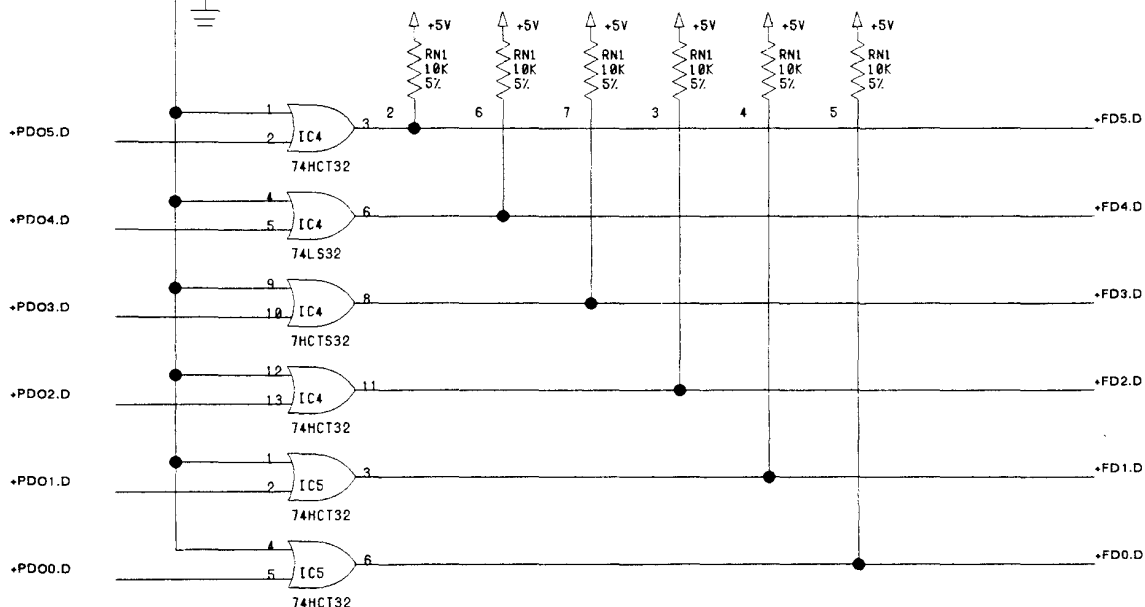
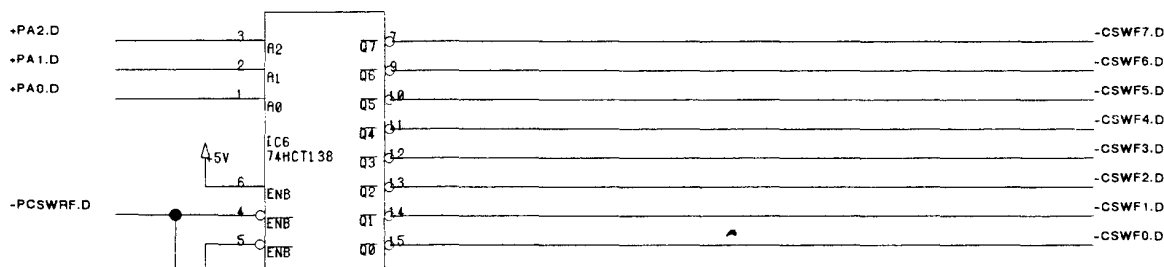
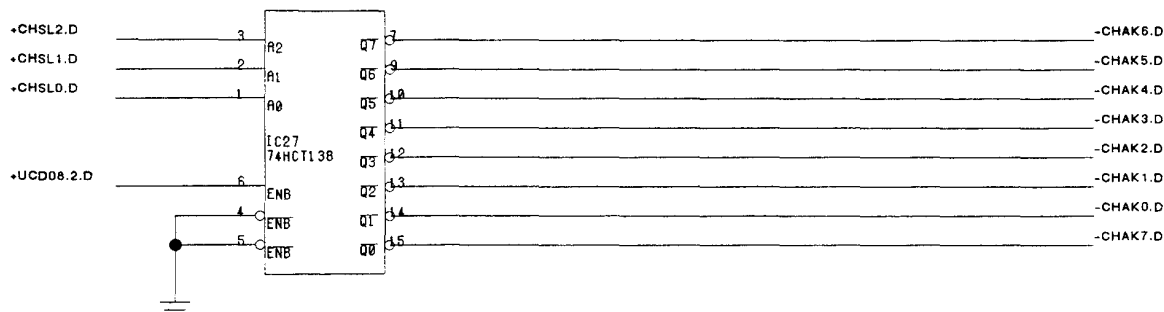
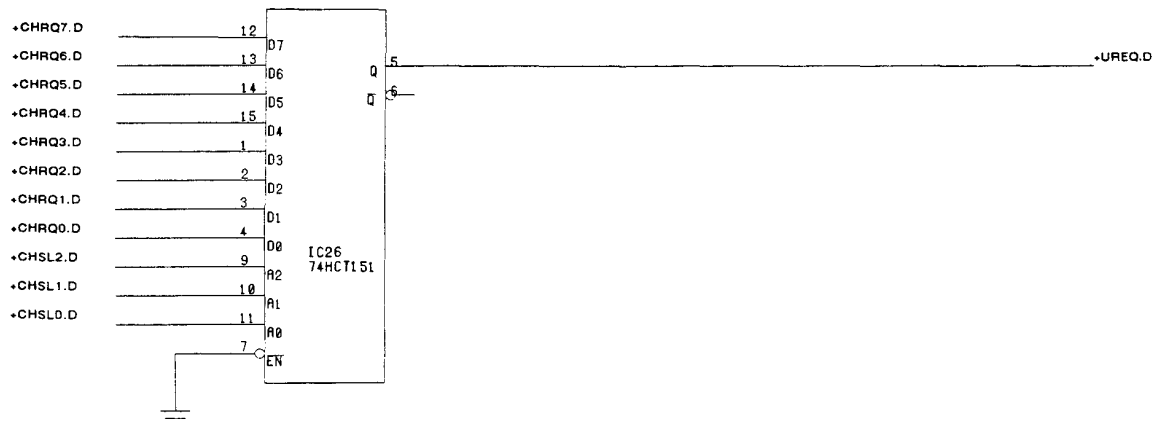
**E-mu Systems, Inc.**

Emulator 2 Output: Timers

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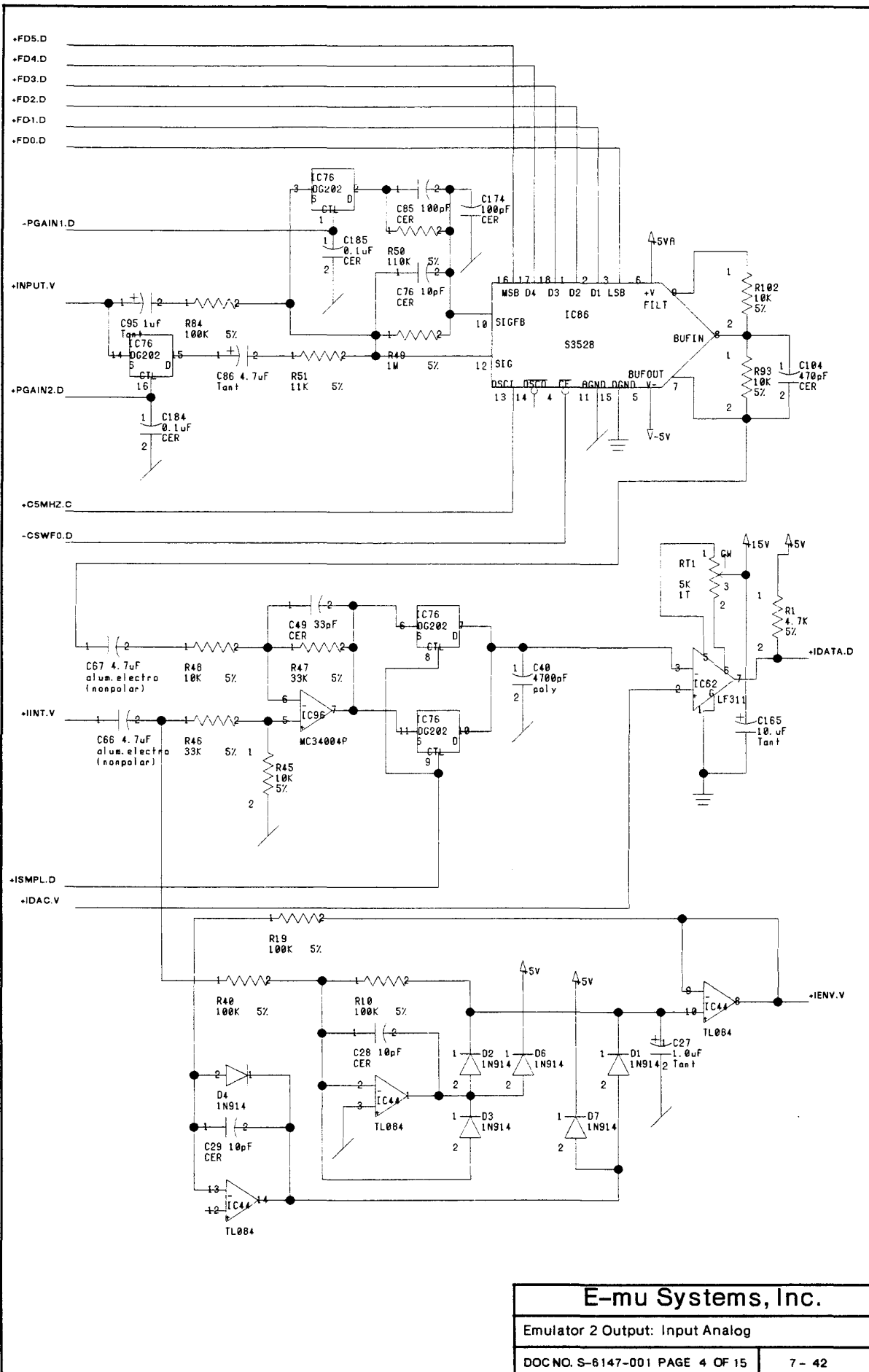


E-mu Systems, Inc.

Emulator 2 Output: RQ/AK, Filter Sel

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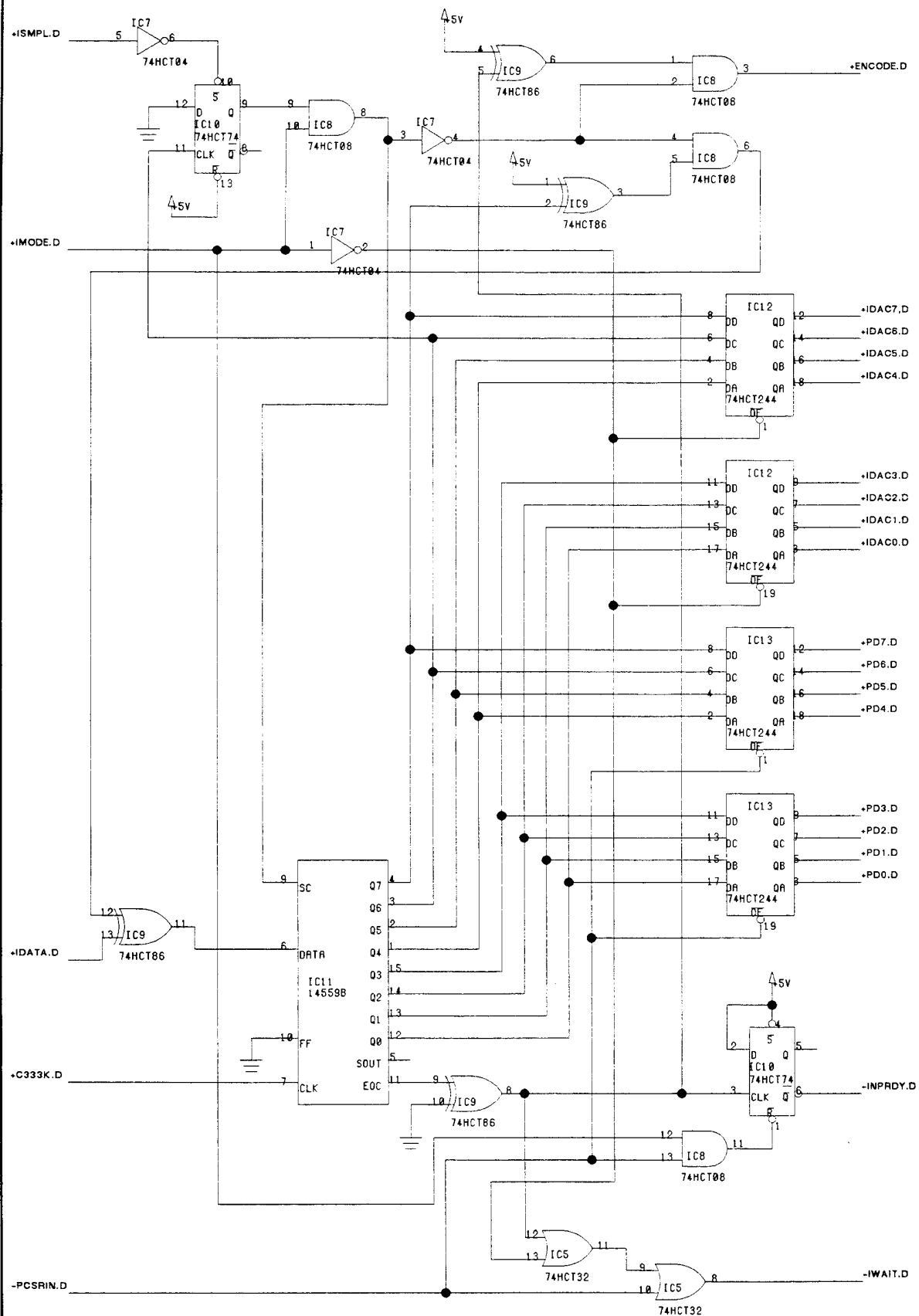


E-mu Systems, Inc.

Emulator 2 Output: Input Analog

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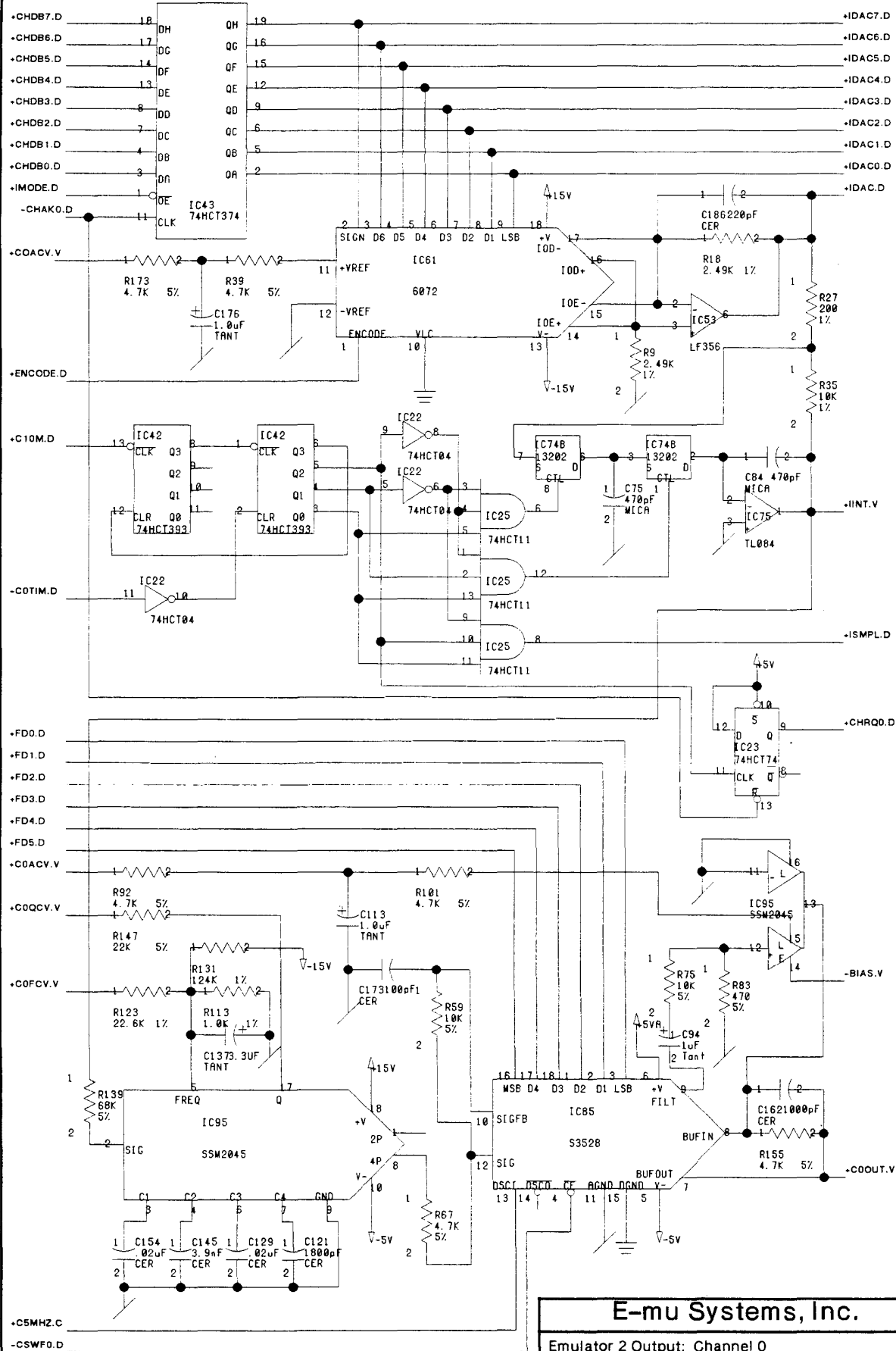
E-mu Systems, Inc.

Emulator 2 Output: SAR and logic

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NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN



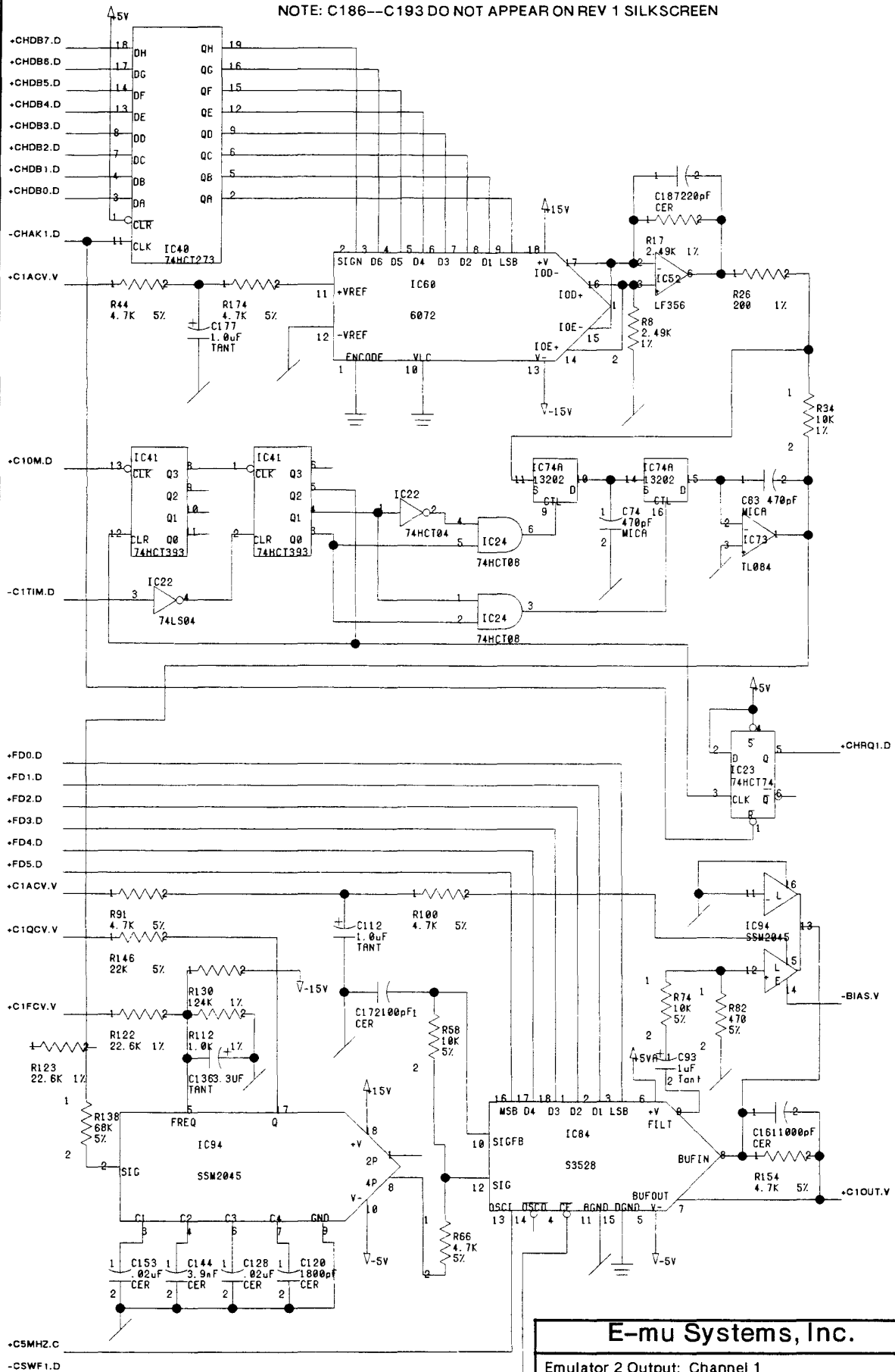
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Emulator 2 Output: Channel 0

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NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN



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Emulator 2 Output: Channel 1

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NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN

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+CHDB7.D 18 DH QH 19  
+CHDB6.D 17 DG QG 16  
+CHDB5.D 14 DF QF 15  
+CHDB4.D 13 DE QE 12  
+CHDB3.D 8 DD QD 9  
+CHDB2.D 7 DC QC 6  
+CHDB1.D 4 DB QB 5  
+CHDB0.D 3 DA QA 2  
-CHAK2.D 1 CLR  
CLK IC39 74HCT273

+C2ACV.V 1 R38 4.7K 5% 2 R175 4.7K 5% 3 C178 1.0uF TANT 4  
+C10M.D 13 IC38 5 Q3 6 IC38 5 Q3 6  
CLR Q0 1 CLR Q0 1  
-C2TIM.D 13 IC19 12 74HCT04

15V  
IC59 6072  
+VREF 11  
-VREF 12  
ENCODE 1  
VLC 10  
SIGN 2 D6 3 D5 4 D4 5 D3 6 D2 7 D1 8 LSB 9  
+V 17  
IO0- 16  
IO0+ 15  
IOE- 14  
IOE+ 13  
R16 2.49K 1%  
C188 220pF CER  
R25 200 1%  
R33 10K 1%  
R7 2.49K 1%  
LF356  
IC51  
C73 470pF MICA  
C72 TL084

IC38 74HCT393  
IC19 74HCT04  
IC21 74HCT08  
IC71B 3202  
C82 470pF MICA  
IC72 TL084

5V  
+CHQ2.D 12 D Q 9  
IC20 74HCT74  
CLK Q 8  
R 13

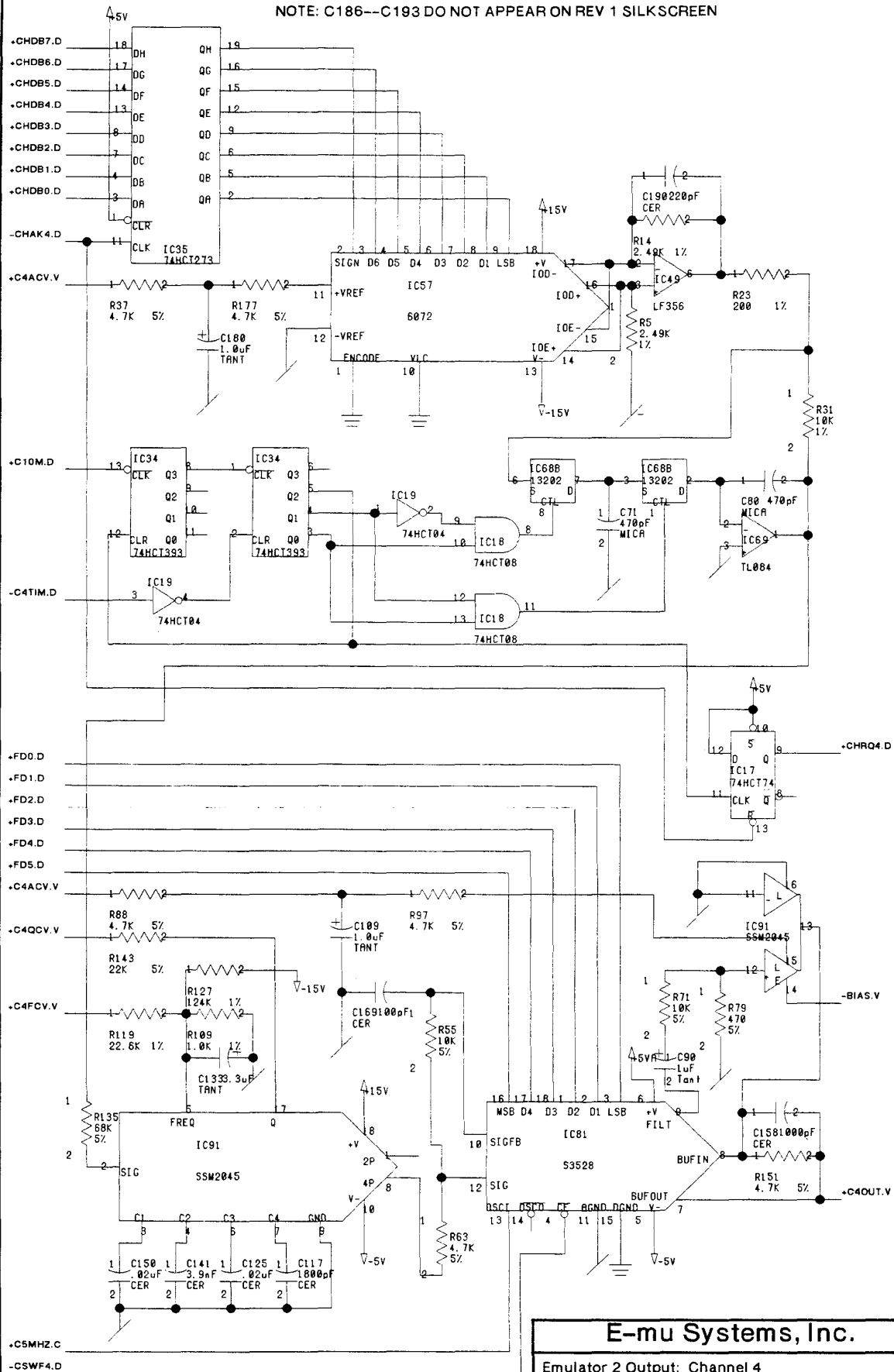
+FD0.D  
+FD1.D  
+FD2.D  
+FD3.D  
+FD4.D  
+FD5.D  
+C2ACV.V 1 R90 4.7K 5% 2 R99 4.7K 5% 3 C111 1.0uF TANT 4  
+C2QCV.V 1 R145 22K 5% 2 R129 124K 1% 3 R111 1.0K 1% 4 C1353 3uF TANT 5  
+C2FCV.V 1 R121 22.6K 1% 2 R137 68K 5% 3  
FREQ 6 Q 7  
IC93 SSM2045  
+V 18  
2P 19  
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NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN



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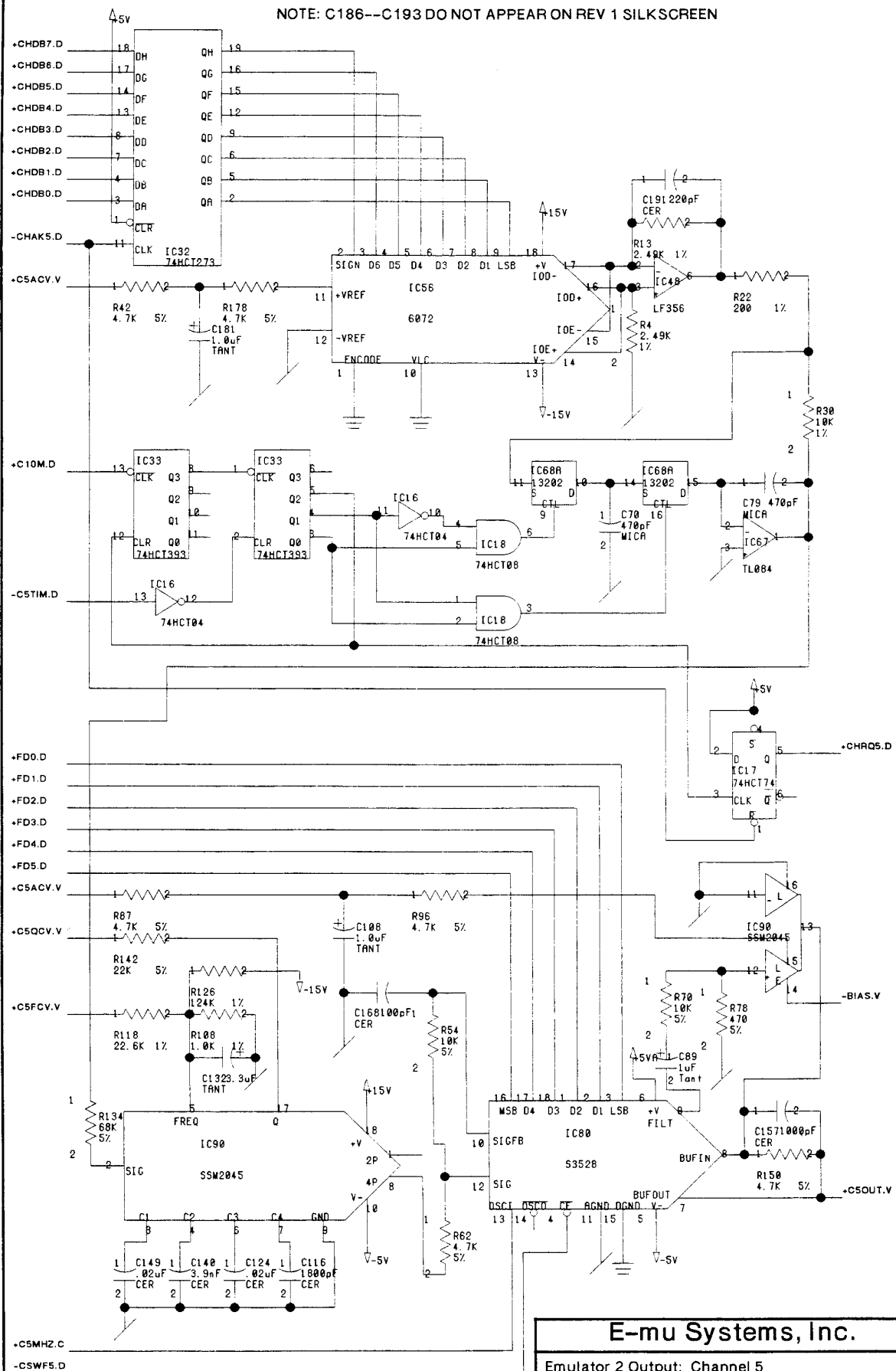
Emulator 2 Output: Channel 4

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NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN



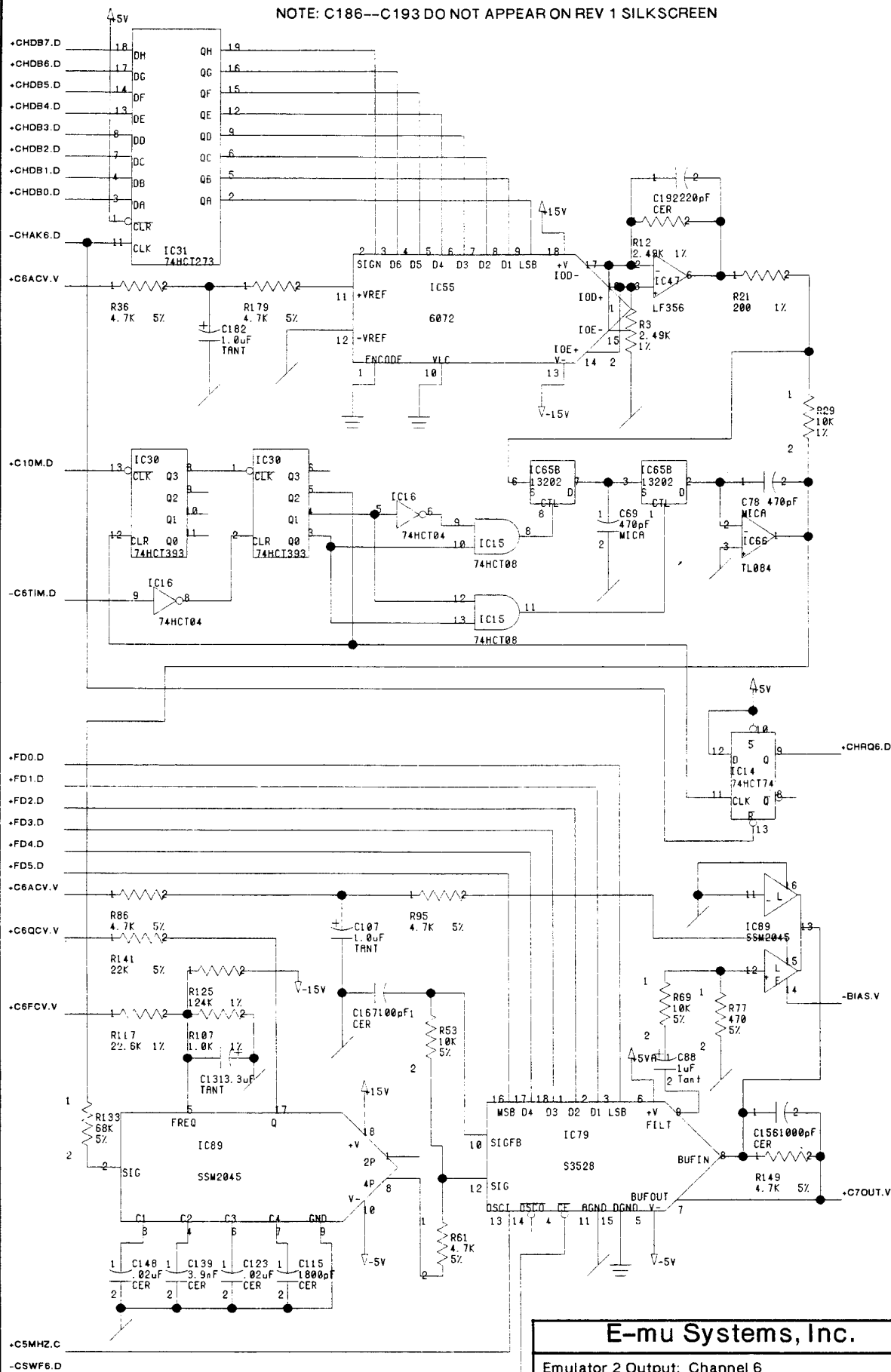
E-mu Systems, Inc.

Emulator 2 Output: Channel 5

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7 - 49

NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN



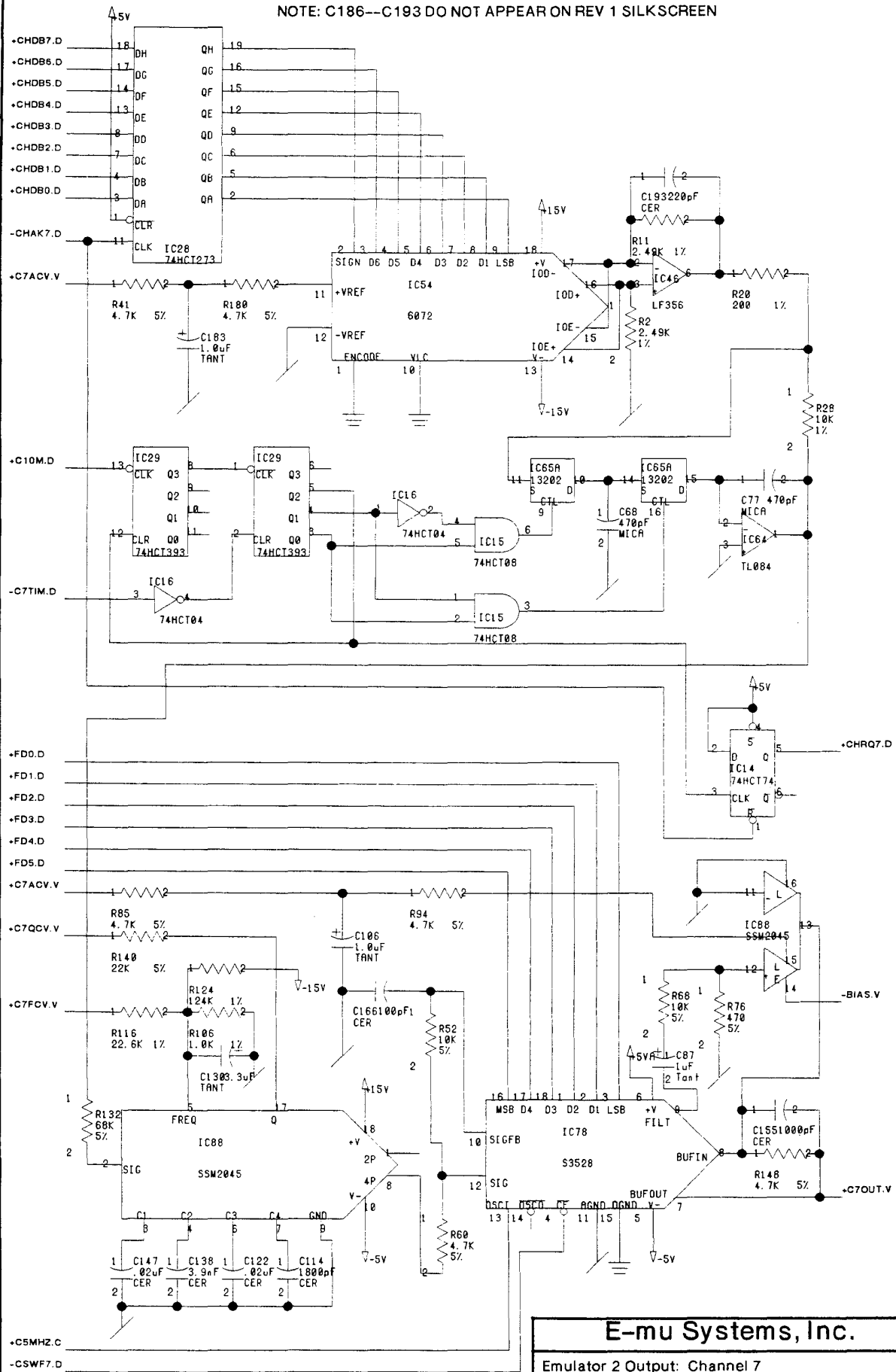
E-mu Systems, Inc.

Emulator 2 Output: Channel 6

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7 - 50

NOTE: C186--C193 DO NOT APPEAR ON REV 1 SILKSCREEN

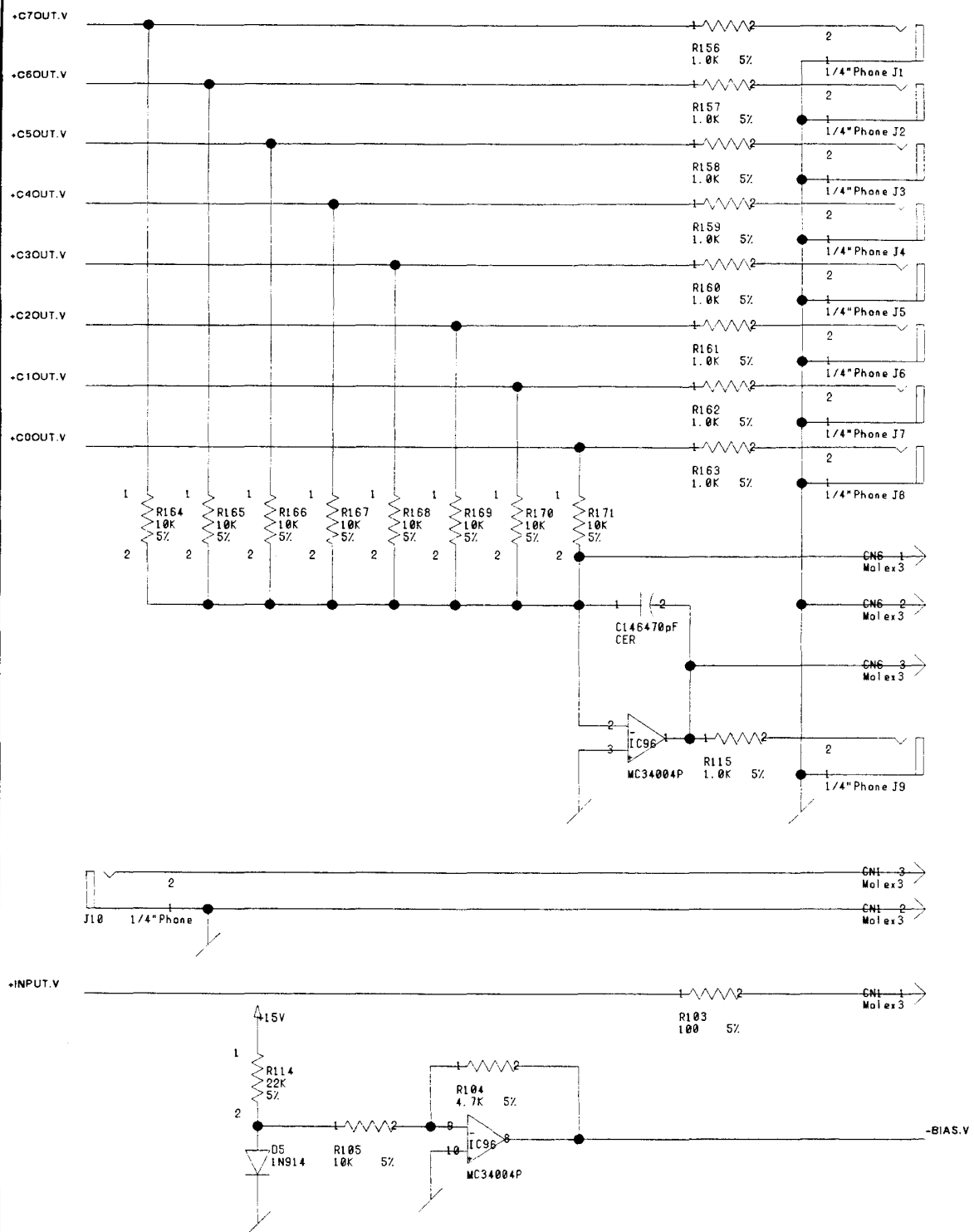


E-mu Systems, Inc.

Emulator 2 Output: Channel 7

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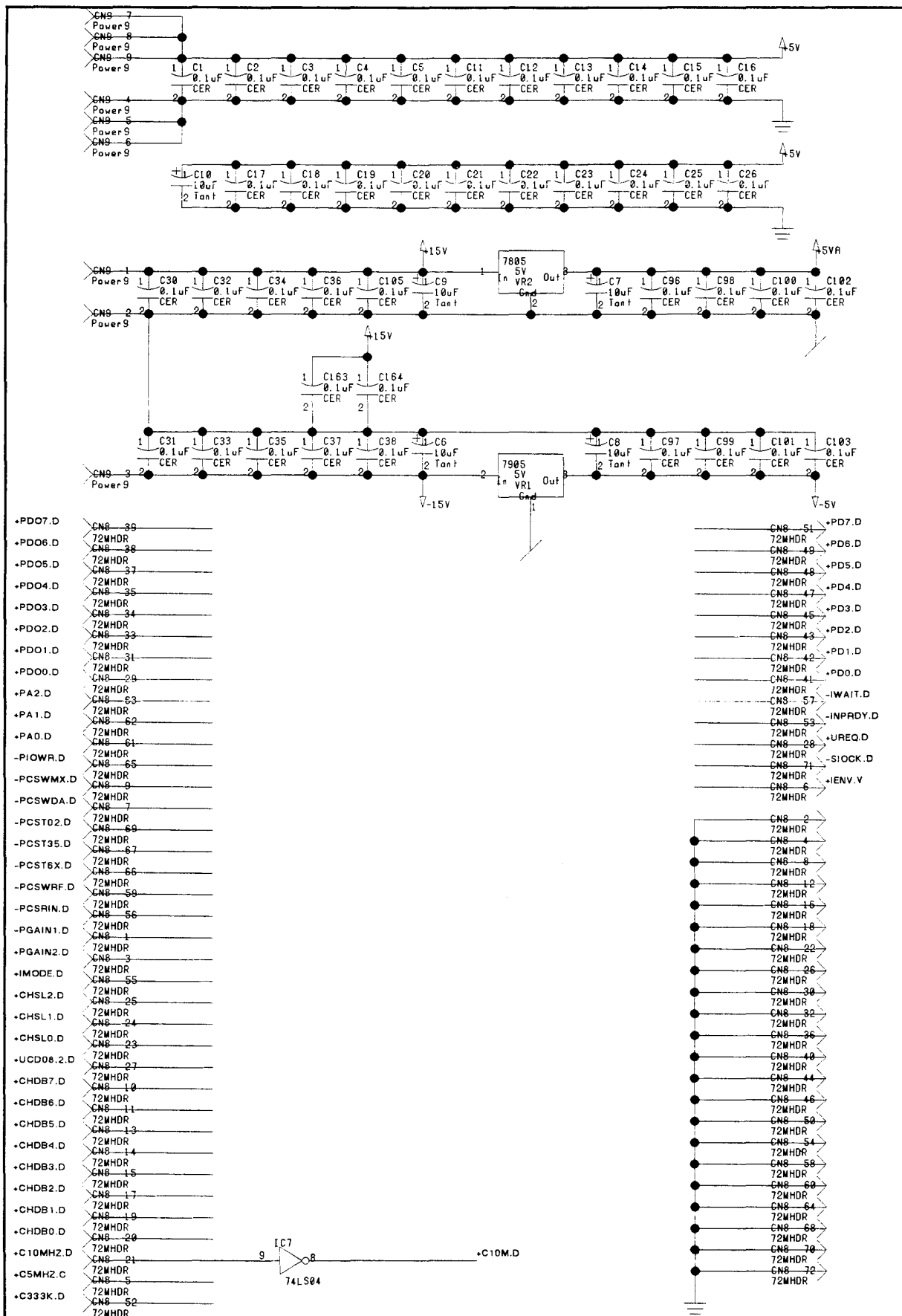


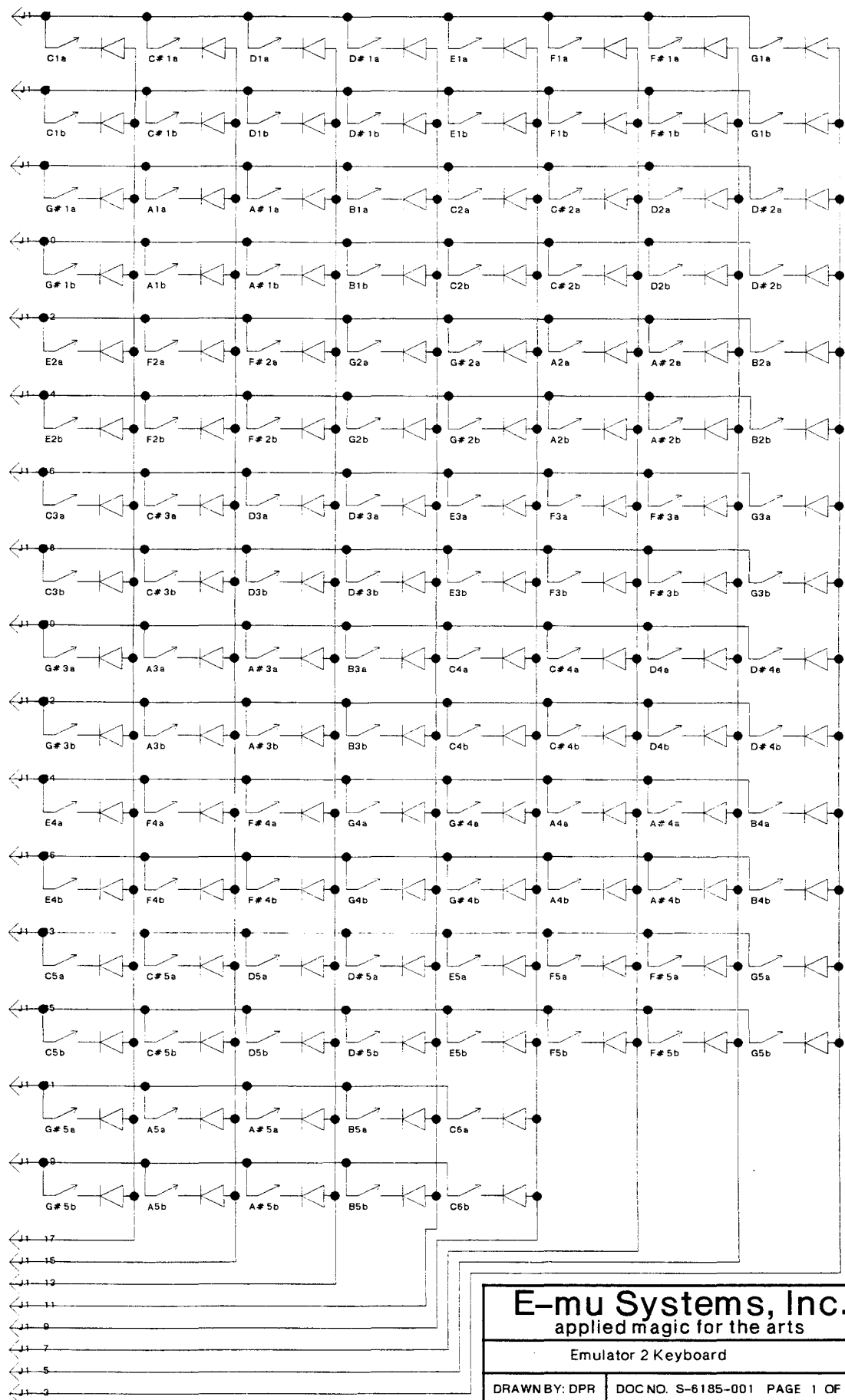
E-mu Systems, Inc.

Emulator 2 Output: Mixer, Common

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**E-mu Systems, Inc.**  
applied magic for the arts

Emulator 2 Keyboard

DRAWN BY: DPR

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